Rev. 04.27.09 LGA C SERIES

LGA C Series

15-100 Watts

Total Power: 15-100 Watts No. of Outputs: Single

Special Features

- 3,6,10 and 20 A output
- Wide input voltage range;
- Adjustable output voltage;

- Output margining

- Remote sense

- up to 13.8 V

Safety

Solderability: J-STD-002B IEC-60068-2-58

Electrical Specifications

Output		3/6/10 A Models	20 A Model	
Output voltage	See Page 3		0-5.1 V	
Output setpoint accuracy	0.1% trim resistors	-	.0%	
Line regulation		+0	.2%	
Load regulation			.5%	
Max Current Max Power		15/30/50 W	100 W	
Overshoot	At turn-on)%	
Undershoot	At turn-off	0	mV	
Ripple and noise 5 Hz to 20 MHz	See Note 1 V _{in} = 5 V, V _{out} = 2.5 V	20/25/30 mV	30 mV	
Transient response	See Notes 1 and 2 V _{in} = 5 V, V _{out} = 2.5 V	100/160/160 mV 15 µs recovery to within regulation band	175 mV 15 µs recovery to within regulation band	
Input				
Input voltage range		3-13.8 Vdc	4.5-13.8 Vdc	
Input current	Enable On at (0 A) Enable Off		mA mA	
Start-up time	Power up Enable On/Off	3 ms 2 ms		
General				
Efficiency	V _{in} =5 V _{out} , Vo=2.5 V, I _{out} = 50% Imax	92%	б Тур.	
Switching frequency		1 MHz	800 kHz	
Material flammability		UL94V-0		
MTBF	12 V @ 40 °C 100% load Bellcore 332	> 20,000,000 hours		
Coplanarity		150) μm	



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Electrical Specifications (cont'd)

Thermal performance See Technical Reference Note	Operating ambient Non-operating ambient	-40 °C to +85 °C -40 °C to +125 °C
Protection		
Short circuit		Hiccup, non-latching
Overvoltage		Hiccup, non-latching

Mininum Recommended System Capacitance	3/6/10 A Model	20 A Model
Input capacitance	1 μF	10 μF
Output capacitance	10 μF	50 μF

Operating	Information							
Output Power (Max.)	Input Voltage	Output Voltage	Output Current (Min.)	Output Current (Max.)	Efficiency (Typical)	Regu Line	lation Load	Standard Model Numbers
15 W	3-13.8 Vdc	0.59-5.1 Vdc	0 A	3 A	92%	±0.2%	±0.5%	LGA03C-00SADJJ
30 W	3-13.8 Vdc	0.59-5.1 Vdc	0 A	6 A	92%	±0.2%	±0.5%	LGA06C-00SADJJ
50 W	3-13.8 Vdc	0.59-5.1 Vdc	0 A	10 A	92%	±0.2%	±0.5%	LGA10C-00SADJJ
100 W	4.5-13.8 Vdc	0.59-5.1 Vdc	0 A	20 A	91%	±0.2%	±0.5%	LGA20C-01SADJJ

Model Number System with Options



Product Family	Rated Output Current	Performance	Input Voltage	Type of Output	Options	RoHS Compliance
LGA	XX	C	- 00	SADJ	X	J
	Rated Output Current 03 = 3 Amp 06 = 6 Amp 10 = 10 Amp 20 = 20 Amp	Performance C = Cost Optimized	Input Voltage 00 = 3-13.8 V 01 = 4.5-13.8 V	Type of Output Single Adjustable Output	Options X = Various Options (see Sales Rep)	RoHS Compliance J = Pb free (RoHS 6/6 compliant)

Heatsink Number System with Options



Product Family	Product	Purpose	Height*
LGA	- HTSK	- KIT	- XXX
Land Grid Array	Heatsink	Heatsink and Adhesive	Total Height (LGA20 + Heatsink) 045 = 0.45" 048 = 0.48" 050 = 0.50"

^{*}Height is the total height of the LGA20C-00SADJJ with heatsink attached.

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Application Equations

Setting Output Voltage

Default output voltage: 0.591 V

The outut voltage may be adjusted with a resistor placed between the "Trim" and "-Sense" pin.

The formula for calcuating the value of this resistor is:

$$R_{trim}(k\Omega) = \frac{1.182}{V_{out} - 0.591}$$

See Technical Reference Note for other trimming methods.

Setting Under Voltage Lock Out - 3, 6, 10 A Models

Default Turn-on voltage: 2.9 V (300 mV Hysteresis)

The Turn-on voltage may be adjusted with a resistor placed between the "Enable" and "Ground" pins.

The formula for calculating the value of this resistor is:

$$R_{UV|O}(k\Omega) = \frac{14.81 * 6.81}{(6.81 * V_{Turn_on}) - 18.16}$$

*ONLY USE WITH OPEN COLLECTOR DEVICE

*DO NOT DRIVE PIN WITH A VOLTAGE

Setting Under Voltage Lock Out - 15 and 20 A Models

Default Turn-on voltage: 4.3 V (300 mV Hysteresis)

The Turn-on voltage may be adjusted with a resistor placed between the "Enable" and "Ground" pins.

The formula for calculating the value of this resistor is:

$$R_{UV|O}(k\Omega) = \frac{30.1 * 4.22}{(8.577 * V_{Turn}_{On}) - 34.32}$$

*ONLY USE WITH OPEN COLLECTOR DEVICE

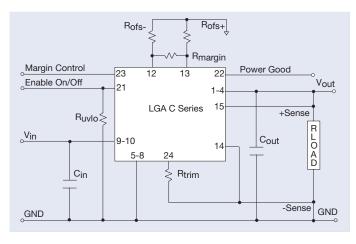
*DO NOT DRIVE PIN WITH A VOLTAGE

Setting Margin Control

To margin the output up, pull the margin control pin high. To margin down, pull the margin control pin low. If the pin is left floating, the feature is disabled. The maximum margining range is $\pm 200 \text{mV}$ at the default voltage setting. The equations for margining up and down are as follows:

$$V_{margin_up} = 0.1182 * \frac{R_{margin}}{R_{ofs}} * \frac{R_{trim} + 2k}{R_{trim}}$$

$$V_{margin_down} = 0.1182 * \frac{R_{margin}}{R_{ofs-}} * \frac{R_{trim} + 2k}{R_{trim}}$$



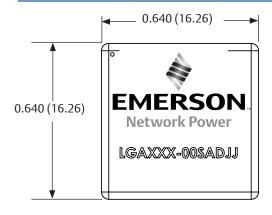
External input fusing is recommended.

Notes:

- 1. Measured as per recommended minimum system capacitance.
- 2. di/dt = 10 A/ μs,12 Vin = Norm, Tc = 25 °C, load change = 50% lo 100% lmax.

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Mechanical Drawing and Footprint



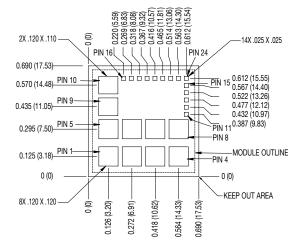


Component Height		
Model # DIM A in (mm)		
LGA03 LGA06 LGA10	0.129 (3.27)	
LGA20	0.210 (5.33)	

Recommended System Board Footprint

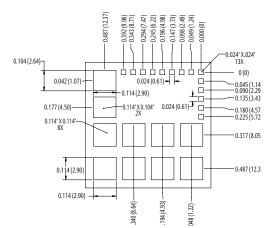
Pin Assignments Single Output 1 Vout 2 Vout 3 Vout 4 Vout 5 **GND** 6 **GND** 7 **GND** 8 **GND** 9 Vin 10 Vin 11 NC - Offset 12 + Offset 13 14 - Sense 15 + Sense 16 NC 17 NC 18 NC 19 NC 20 NC 21 Enable 22 Power Good 23 Margin Control 24 Trim

*This is a Preliminary Datasheet. Emerson Network Power reserves the right to make changes to the information contained herein without notice and assumes no liability as a result of its use or application.



Tolerance Note: ±0.010 (0.25)

Recommended Solder Paste Stencil



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