ERICSSON

Technical Specification

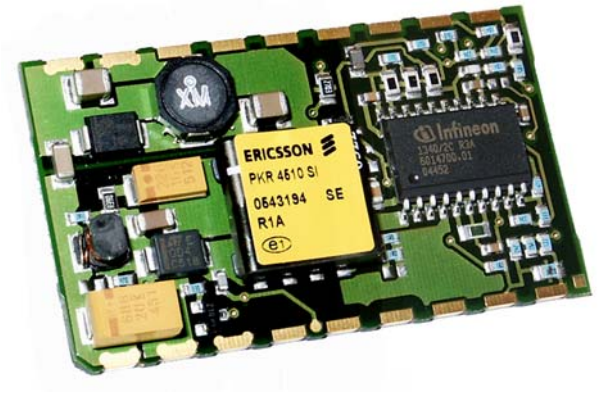
PKR 4000 SI series DC/DC converters, Input 36-75 V, Output 1.5 A/7 W	EN/LZT 146 302 R5A January 2006

Key Features

- Industry standard MacroDens™ footprint 47.8 x 28.1 x max height 8.0 mm (1.88 x 1.11 x max height 0.32 in.)
- High efficiency, typ. 83 % at 5.0 Vout full load
- 1500 Vdc input to output isolation
- Meets isolation requirements equivalent to basic insulation according to IEC/EN/UL 60950
- More than 7.2 million hours predicted MTBF at 40°C ambient temperature

General Characteristics

- Suited for narrow board pitch applications (15 mm/0.6 in)
- Over current protection
- Soft start
- Remote control
- Output voltage adjust function
- Input voltage adjust function
- Highly automated manufacturing to ensure highest quality
- ISO 9001/14001 certified supplier



Safety Approvals

c UL us

Design for Environment

Pb RoHS compliant

Meets requirements in high-temperature lead-free soldering processes.

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General Information

Ordering Information

See Contents for individual product ordering numbers.

Reliability

The Mean Time Between Failure (MTBF) is calculated at full output power and an operating ambient temperature (T_A) of +40°C. Different methods could be used to calculate the predicted MTBF and failure rate which may give different results. Ericsson Power Modules currently uses one method, Telcordia SR332.

Predicted MTBF for the series is:
- 7.2 million hours according to Telcordia SR332, issue 1, Black box technique.

The Ericsson failure rate data system is based on field tracking data. The data corresponds to actual failure rates of components used in Information Technology and Telecom (IT&T) equipment in temperature controlled environments
(T_A = -5...+65°C). Telcordia SR332 is a commonly used standard method intended for reliability calculations in IT&T equipment. The parts count procedure used in this method was originally modelled on the methods from MIL-HDBK-217F, Reliability Predictions of Electronic Equipment.
It assumes that no reliability data is available on the actual units and devices for which the predictions are to be made, i.e. all predictions are based on generic reliability parameters.

Design for environment (DfE)

The products are designed to fulfil the wanted functionality with minimum environmental impact. The design process objectives are to minimize power consumption by high efficiency electrical solutions and to avoid or minimize the materials listed in the RoHS directive, with special focus on lead-free components, lead-free manufacturing processes and to meet the requirement in customers lead-free manufacturing processes.

The products are compatible with the relevant clauses and requirements of the RoHS directive 2002/95/EC and have a maximum concentration value of 0.1% by weight in homogeneous materials for Lead in other applications other than lead in solder, lead in high melting temperature type solder, lead in glass of electronics components, lead in electronic ceramic parts and lead as an alloying element in copper containing up to 4% lead by weight, Mercury, Hexavalent Chromium, PBB and PBDE and of 0.01% by weight in homogeneous materials for Cadmium.

Exemptions in the RoHS directive utilized in the products:
- Lead as an alloying element in copper alloy containing up to 4% lead by weight
- Lead in high melting temperature type solder
- Lead in glass of electronics components

Lead in solder for servers, storage and storage array systems, network infrastructure equipment for switching, signaling, transmission as well as network management for telecommunication

Quality Statement

The products are designed and manufactured in an industrial environment where quality systems and methods like ISO 9000, 6σ (sigma), and SPC are intensively in use to boost the continuous improvements strategy. Infant mortality or early failures in the products are screened out and they are subjected to an ATE-based final test. Conservative design rules, design reviews and product qualifications, plus the high competence of an engaged work force, contribute to the high quality of our products.

Warranty

Warranty period and conditions are defined in Ericsson Power Modules General Terms and Conditions of Sale.

Limitation of Liability

Ericsson power Modules does not make any other warranties, expressed or implied including any warranty of merchantability or fitness for a particular purpose (including, but not limited to, use in life support applications, where malfunctions of product can cause injury to a person’s health or life).

Safety Specification

General information
Ericsson Power Modules DC/DC converters and DC/DC regulators are designed in accordance with safety standards IEC/EN/UL60950, *Safety of Information Technology Equipment*.

IEC/EN/UL60950 contains requirements to prevent injury or damage due to the following hazards:

- Electrical shock
- Energy hazards
- Fire
- Mechanical and heat hazards
- Radiation hazards
- Chemical hazards

On-board DC-DC converters are defined as component power supplies. As components they cannot fully comply with the provisions of any Safety requirements without "Conditions of Acceptability". It is the responsibility of the installer to ensure that the final product housing these components complies with the requirements of all applicable Safety standards and Directives for the final product.

Component power supplies for general use should comply with the requirements in IEC60950, EN60950 and UL60950 *"Safety of information technology equipment".*

There are other more product related standards, e.g. IEEE802.3af "Ethernet LAN/MAN Data terminal equipment power", and ETS300132-2 "Power supply interface at the input to telecommunications equipment; part 2: DC", but all of these standards are based on IEC/EN/UL60950 with regards to safety.

Ericsson Power Modules DC/DC converters and DC/DC regulators are UL60950 recognized and certified in accordance with EN60950.

The flammability rating for all construction parts of the products meets requirements for V-0 class material according to IEC 60695-11-10.

The products should be installed in the end-use equipment, in accordance with the requirements of the ultimate application. Normally the output of the DC/DC converter is considered as SELV (Safety Extra Low Voltage) and the input source must be isolated by minimum Double or Reinforced Insulation from the primary circuit (AC mains) in accordance with IEC/EN/UL60950.

Isolated DC/DC converters
It is recommended that a slow blow fuse with a rating twice the maximum input current per selected product be used at the input of each DC/DC converter. If an input filter is used in the circuit the fuse should be placed in front of the input filter.
In the rare event of a component problem in the input filter or in the DC/DC converter that imposes a short circuit on the input source, this fuse will provide the following functions:

- Isolate the faulty DC/DC converter from the input power source so as not to affect the operation of other parts of the system.
- Protect the distribution wiring from excessive current and power loss thus preventing hazardous overheating.

The galvanic isolation is verified in an electric strength test. The test voltage (V_{iso}) between input and output is 1500 Vdc or 2250 Vdc for 60 seconds (refer to product specification).
Leakage current is less than 1 µA at nominal input voltage.

24 V DC systems
The input voltage to the DC/DC converter is SELV (Safety Extra Low Voltage) and the output remains SELV under normal and abnormal operating conditions.

48 and 60 V DC systems
If the input voltage to Ericsson Power Modules DC/DC converter is 75 Vdc or less, then the output remains SELV (Safety Extra Low Voltage) under normal and abnormal operating conditions.

Single fault testing in the input power supply circuit should be performed with the DC/DC converter connected to demonstrate that the input voltage does not exceed 75 Vdc.

If the input power source circuit is a DC power system, the source may be treated as a TNV2 circuit and testing has demonstrated compliance with SELV limits and isolation requirements equivalent to Basic Insulation in accordance with IEC/EN/UL60950.

Non-isolated DC/DC regulators
The input voltage to the DC/DC regulator is SELV (Safety Extra Low Voltage) and the output remains SELV under normal and abnormal operating conditions.

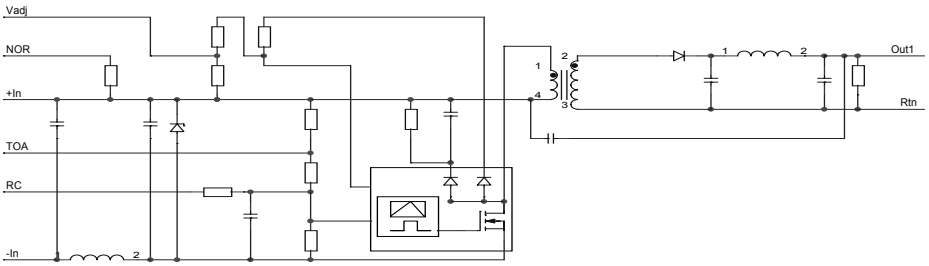
Absolute Maximum Ratings

Characteristics			min	typ	max	Unit
T_{ref}	Operating Temperature (see Thermal Consideration section)		-45		+110	°C
T_S	Storage temperature		-55		+125	°C
V_I	Input voltage		-0.5		+75	V
V_{iso}	Isolation voltage (input to output test voltage)				1500	Vdc
V_{tr}	Input voltage transient (Tp 100 ms)				100	V
V_{RC}	Remote Control pin voltage (see Operating Information section)	Positive logic option	-5		+40	V
V_{adj}	Adjust pin voltage (see Operating Information section)		-5		+40	V

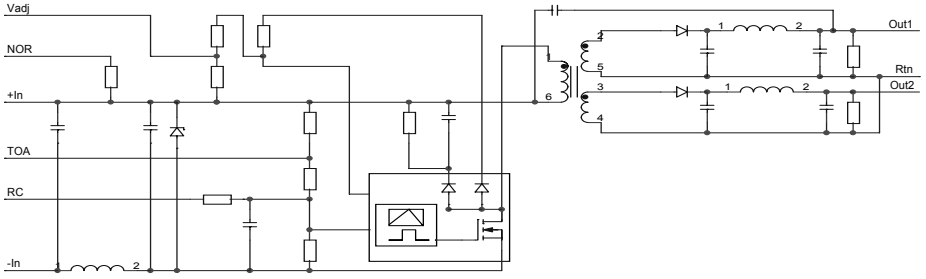
Stress in excess of Absolute Maximum Ratings may cause permanent damage. Absolute Maximum Ratings, sometimes referred to as no destruction limits, are normally tested with one parameter at a time exceeding the limits of Output data or Electrical Characteristics. If exposed to stress above these limits, function and performance may degrade in an unspecified manner.

Fundamental Circuit Diagram

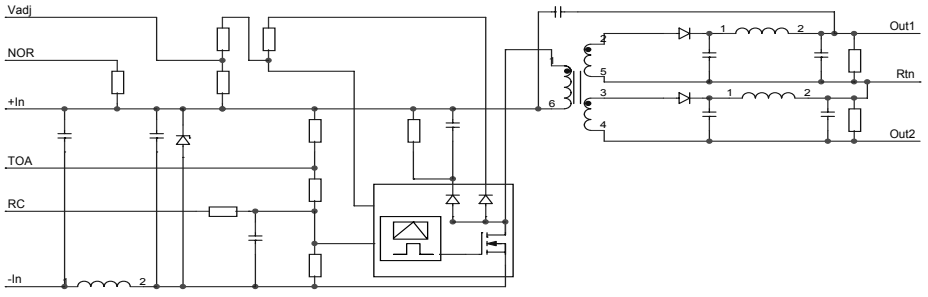
Single output



Double output (positive output 2)



Double output (negative output 2)



2.1 V/1.5 A Electrical Specification — PKR 4310 SI

T_{ref} = -30 to +85ºC, V_I = 38 to 72 V, pin 8 connected to pin 9 unless otherwise specified under Conditions.
Typical values given at: T_{ref} = +25°C, V_I= 53 V, max I_O , unless otherwise specified under Conditions.

Characteristics		Conditions	min	typ	max	Unit
V_I	Input voltage range		38		72	V
V_{Ioff}	Turn-off input voltage	Decreasing input voltage	30	33.4	36	V
V_{Ion}	Turn-on input voltage	Increasing input voltage	32	34.8	38	V
C_I	Internal input capacitance			2		µF
P_O	Output power	Output voltage initial setting	0		3.2	W
SVR	Supply voltage rejection (ac)	f = 100 Hz sinewave, 1 Vp-p		70		dB
η	Efficiency	50 % of max I_O		75		%
		max I_O		75		
		50 % of max I_O , V_I = 48 V		75		
		max I_O , V_I = 48 V		75		
P_d	Power Dissipation	max I_O		1.0	1.4	W
P_{li}	Input idling power	I_O= 0, V_I = 53 V		70		mW
P_{RC}	Input standby power	V_I = 53 V (turned off with RC)		34		mW
f_s	Switching frequency	50-100% of max I_O	412	485	558	kHz

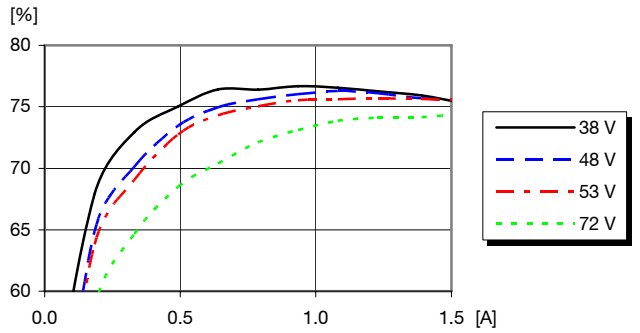
V_{Oi}	Output voltage initial setting and accuracy	T_{ref} = +25°C, V_I = 53 V, I_O = 1.15A	2.07	2.12	2.17	V
	Output adjust range		1.76		2.38	V
V_O	Output voltage tolerance band	10-100% of max I_O	2.01		2.28	V
	Idling voltage	I_O = 0		2.5	3.0	V
	Line regulation	max I_O		14	28	mV
	Load regulation	V_I = 53 V, 10-100% of max I_O		130	185	mV
V_{tr}	Load transient voltage deviation	V_I = 53 V, Load step 25-75-25 % of max I_O, di/dt = 1 A/µs, see Note 1		-225 +90		mV
t_{tr}	Load transient recovery time			100		us
t_r	Ramp-up time (from 10–90 % of V_{Oi})	10-100% of max I_O	0.3	0.7	0.9	ms
t_s	Start-up time (from V_I connection to 90% of V_{Oi})		1.3	2.8	6	ms
I_O	Output current		0		1.5	A
I_{lim}	Current limit threshold	Vo = 1.9 V, T_{ref} < max T_{ref}	2.0	2.6	3.1	A
I_{sc}	Short circuit current	T_{ref} = 25ºC,		2.7	3.1	A
V_{Oac}	Output ripple & noise	See ripple & noise section, max I_O, V_O.		3	50	mVp-p

Note 1: Output filter according to Ripple & Noise section

2.1 V/1.5 A Typical Characteristics

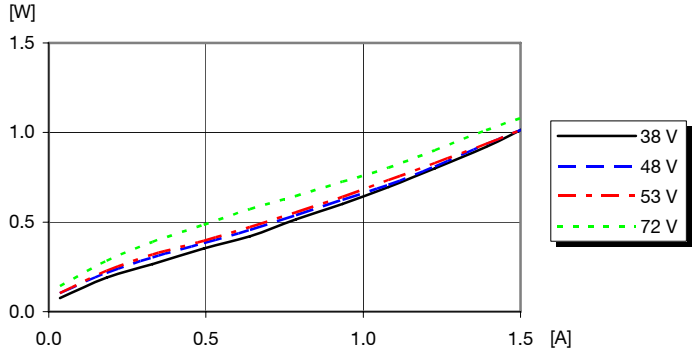
PKR 4310 SI

Efficiency



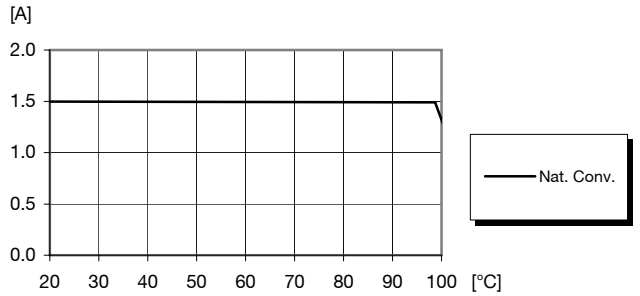
Efficiency vs. load current and input voltage at T_{ref} = +25°C

Power Dissipation



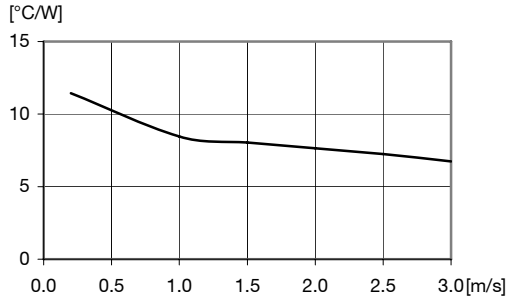
Dissipated power vs. load current and input voltage at T_{ref} = +25°C

Output Current Derating



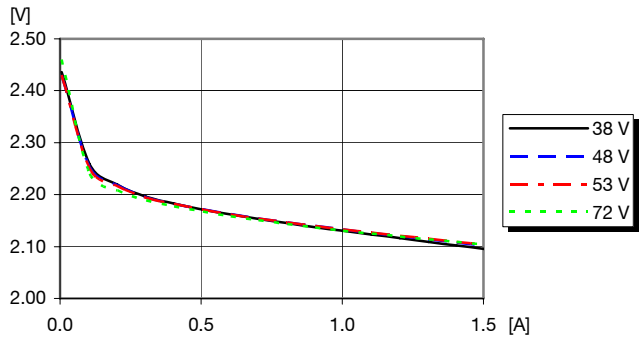
Available load current vs. ambient air temperature and airflow at V_I = 53 V. See Thermal Consideration section.

Thermal Resistance



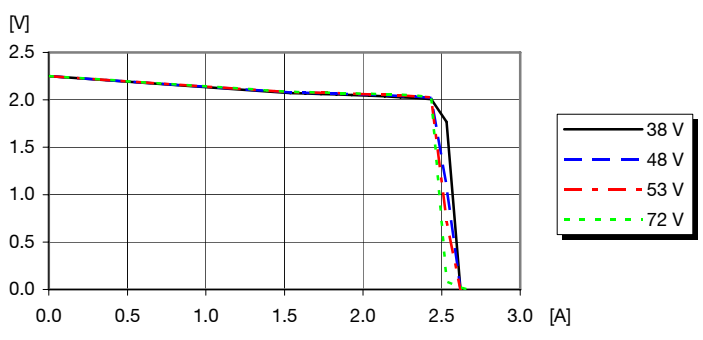
Thermal resistance vs. air speed measured at the converter. Tested in wind tunnel with airflow and test conditions as per the Thermal consideration section.

Output Characteristics



Output voltage vs. load current at T_{ref} = +25°C

Current Limit Characteristics

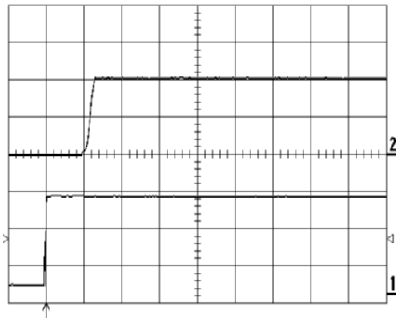


Output voltage vs. load current at I_O > max I_O, T_{ref} = +25°C

2.1 V/1.5 A Typical Characteristics

PKR 4310 SI

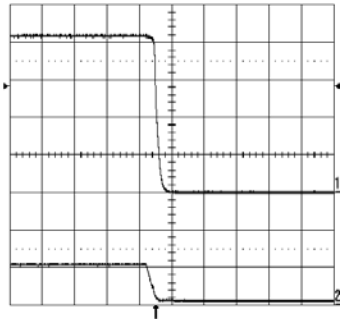
Start-up



Start-up enabled by connecting V_I at: T_{ref} = +25°C, I_O = 1.5 A resistive load, V_I = 53 V.

Top trace: output voltage (1 V/div.). Bottom trace: input voltage (20 V/div.). Time scale: 2 ms/div.

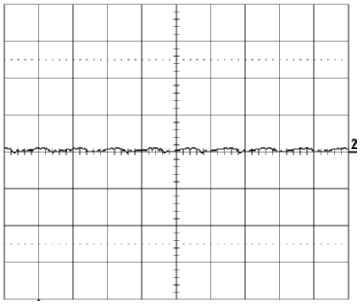
Shut-down



Shut-down enabled by disconnecting V_I at: T_{ref} = +25°C, I_O = 1.5 A resistive load, V_I = 53 V.

Top trace: output voltage (0.5 V/div.). Bottom trace: input voltage (50 V/div.). Time scale: 2 ms/div.

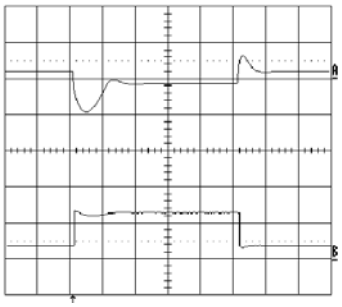
Output Ripple & Noise



Output voltage ripple (20mV/div.) at: T_{ref} = +25°C, I_O = 1.5 A resistive load, V_I = 53 V. Time scale: 2 µs/div.

See the filter in the Output ripple and noise section (EMC Specification).

Output Load Transient Response



Output voltage response to load current step-change (0.38-1.13-0.38 A) at: T_{ref} =+25°C, V_I = 53 V.

Top trace: output voltage (200mV/div.). Bottom trace: load current (1 A/div.). Time scale: 0.1 ms/div.

Output Voltage Adjust (see operating information)

Passive trim
The resistor value for an adjusted output voltage is calculated by using the following equations:

Output Voltage Adjust Upwards, Increase:
R_{ou}= 0.684 x (2.38 – V_o) / (V_o - V_{oi}) kΩ

Eg Increase 4% =>V_0 =2.20 Vdc
0.684 x (2.38 – 2.20) / (2.20 – 2.12) = 1.54 kΩ

Output Voltage Adjust Downwards, Decrease:
R_{od}= 2.751 x (V_{oi} – V_o) / (V_o –1.75) kΩ

Eg Decrease 2% =>V_0 = 2.08Vdc
2.751 x (2.12 – 2.08) / (2.08 –1.75)= *0.33 kΩ*

3.3 V/1.5 A Electrical Specification — PKR 4510 SI

T_{ref} = -30 to +85ºC, V_I = 38 to 72 V, pin 8 connected to pin 9 unless otherwise specified under Conditions.
Typical values given at: T_{ref} = +25°C, V_I= 53 V, max I_O , unless otherwise specified under Conditions.

Characteristics		Conditions	min	typ	max	Unit
V_I	Input voltage range		38		72	V
V_{Ioff}	Turn-off input voltage	Decreasing input voltage	30	33.4	36	V
V_{Ion}	Turn-on input voltage	Increasing input voltage	32	34.8	38	V
C_I	Internal input capacitance			2		µF
P_O	Output power	Output voltage initial setting	0		5	W
SVR	Supply voltage rejection (ac)	f = 100 Hz sine wave, 1 Vp-p		67		dB
η	Efficiency	50 % of max I_O		80.0		%
		max I_O		80.7		
		50 % of max I_O , V_I = 48 V		80.4		
		max I_O , V_I = 48 V		80.7		
P_d	Power Dissipation	max I_O		1.2	2	W
P_{li}	Input idling power	I_O= 0 A, V_I =53 V		92		mW
P_{RC}	Input standby power	V_I = 53 V (turned off with RC)		25		mW
f_s	Switching frequency	50-100% of max I_O	412	485	558	kHz

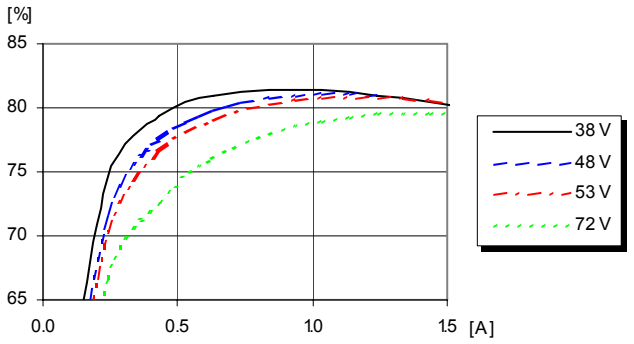
V_{Oi}	Output voltage initial setting and accuracy	T_{ref} = +25°C, V_I = 53 V, max I_O	3.28	3.30	3.32	V
	Output adjust range		2.80		3.80	V
V_O	Output voltage tolerance band	10-100% of max I_O	3.17		3.50	V
	Idling voltage	I_O = 0 A	3.40		4.0	V
	Line regulation	max I_O		3	13	mV
	Load regulation	V_I = 53 V, 10-100% of max I_O		90	220	mV
V_{tr}	Load transient voltage deviation	V_I = 53 V, Load step 25-75-25 % of max I_O, di/dt = 1 A/µs, see Note 1		-190 +100		mV
t_{tr}	Load transient recovery time			0.07		ms
t_r	Ramp-up time (from 10–90 % of V_{Oi})	10-100% of max I_O	0.25	0.8	1.2	ms
t_s	Start-up time (from V_I connection to 90% of V_{Oi})		1	3	9	ms
I_O	Output current		0		1.50	A
I_{lim}	Current limit threshold	V_O = 2.5 V, T_{ref} < max T_{ref}	1.65	2.35	3.30	A
I_{sc}	Short circuit current	T_{ref} = 25ºC		2.40	3.50	A
V_{Oac}	Output ripple & noise	See ripple & noise section, max I_O, V_{Oi}		4	50	mVp-p

Note 1: Output filter according to Ripple & Noise section

3.3 V/1.5 A Typical Characteristics

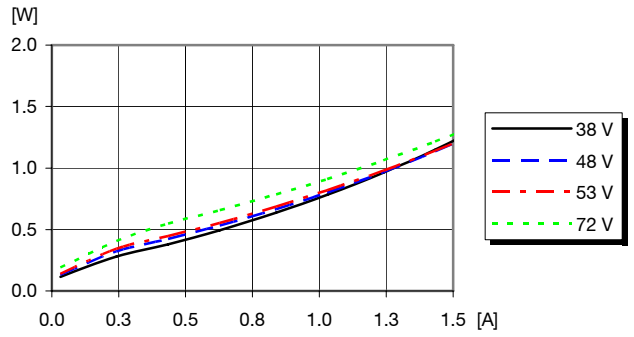
PKR 4510 SI

Efficiency



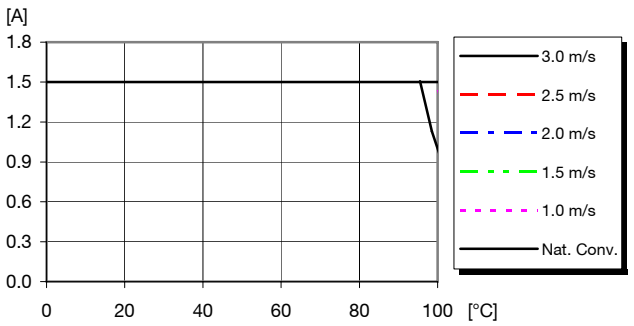
Efficiency vs. load current and input voltage at T_{ref} = +25°C

Power Dissipation



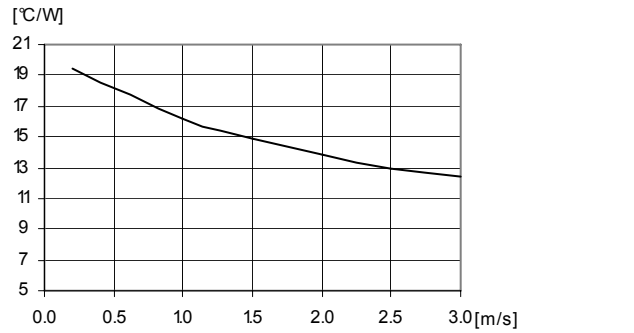
Dissipated power vs. load current and input voltage at T_{ref} = +25°C

Output Current Derating



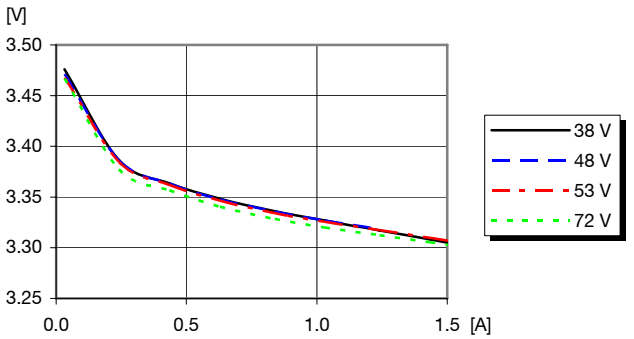
Available load current vs. ambient air temperature and airflow at V_I = 53 V. See Thermal Consideration section.

Thermal Resistance



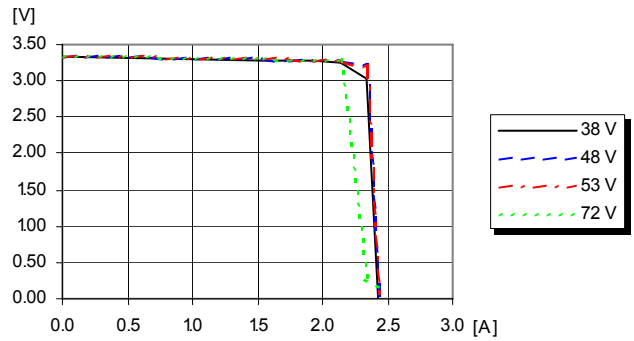
Thermal resistance vs. airspeed measured at the converter. Tested in wind tunnel with airflow and test conditions as per the Thermal consideration section.

Output Characteristics



Output voltage vs. load current at T_{ref} = +25°C

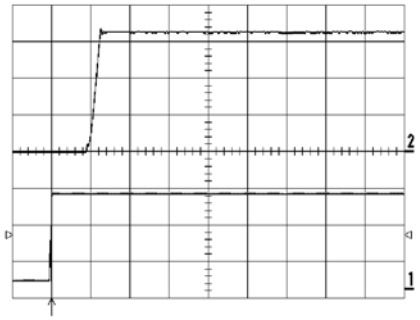
Current Limit Characteristics



Output voltage vs. load current at I_O > max I_O, T_{ref} = +25°C

3.3 V/1.5 A Typical Characteristics — PKR 4510 SI

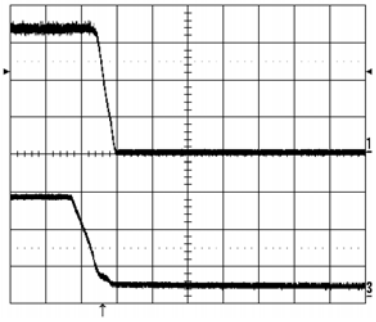
Start-up



Start-up enabled by connecting V_I at: T_{ref} = +25°C, I_O = 1.5 A resistive load, V_I = 53 V.

Top trace: output voltage (1 V/div.). Bottom trace: input voltage (20 V/div.). Time scale: 2 ms/div.

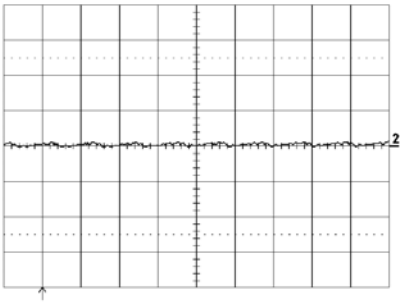
Shut-down



Shut-down enabled by disconnecting V_I at: T_{ref} = +25°C, I_O = 1.5 A resistive load, V_I = 53 V.

Top trace: output voltage (1 V/div.). Bottom trace: input voltage (20 V/div.). Time scale: 0.5 ms/div.

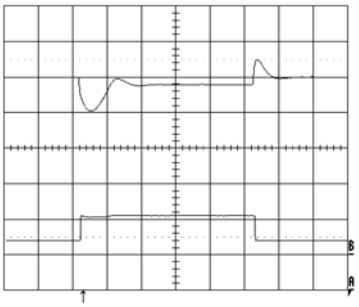
Output Ripple & Noise



Output voltage ripple (20mV/div.) at: T_{ref} = +25°C, I_O = 1.5 A resistive load, V_I = 53 V. Time scale: 2 µs/div.

See the filter in the Output ripple and noise section (EMC Specification).

Output Load Transient Response



Output voltage response to load current step-change (0.38-1.13-0.38 A)) at: T_{ref} =+25°C, V_I = 53 V.

Top trace: output voltage (200mV/div.). Bottom trace: load current (1 A/div.). Time scale: 0.1 ms/div.

Output Voltage Adjust (see operating information)

Passive trim

The resistor value for an adjusted output voltage is calculated by using the following equations:

Output Voltage Adjust Upwards, Increase:

$R_{ou} = 0.495 \times (3.93 - V_O)/(V_O - V_{OI})k\Omega$

E.g. Increase 4% =>V_{out} = 3.43 Vdc

$0.495 \times (3.93 - 3.43)/(3.43 - 3.3) = 1.9\ k\Omega$

Output Voltage Adjust Downwards, Decrease:

$R_{od} = 1.986 \times (V_{OI} - V_O)/(V_O - 2.59)k\Omega$

E.g. Decrease 2% =>V_{out} = 3.23 Vdc

$1.986 \times (3.3 - 3.23)/(3.23 - 2.59) = 0.217\ k\Omega$

5.05 V/1.2 A Electrical Specification — PKR 4611 SI

T_{ref} = -30 to +85ºC, V_I = 38 to 72 V, pin 8 connected to pin 9 unless otherwise specified under Conditions.
Typical values given at: T_{ref} = +25°C, V_I= 53 V, max I_O, unless otherwise specified under Conditions.

Characteristics		Conditions	min	typ	max	Unit
V_I	Input voltage range		38		72	V
V_{Ioff}	Turn-off input voltage	Decreasing input voltage	30	33.4	36	V
V_{Ion}	Turn-on input voltage	Increasing input voltage	32	35	38	V
C_I	Internal input capacitance			2		µF
P_O	Output power	Output voltage initial setting	0		6	W
SVR	Supply voltage rejection (ac)	f = 100 Hz sine wave, 1 Vp-p		68		dB
η	Efficiency	50 % of max I_O		83		%
		max I_O		84		
		50 % of max I_O , V_I = 48 V		83		
		max I_O , V_I = 48 V		84		
P_d	Power Dissipation	max I_O		1.2	1.6	W
P_{li}	Input idling power	I_O = 0 A, V_I = 53 V		0.13		W
P_{RC}	Input standby power	V_I = 53 V (turned off with RC)		26		mW
f_s	Switching frequency	50-100% of max I_O	412	485	558	kHz

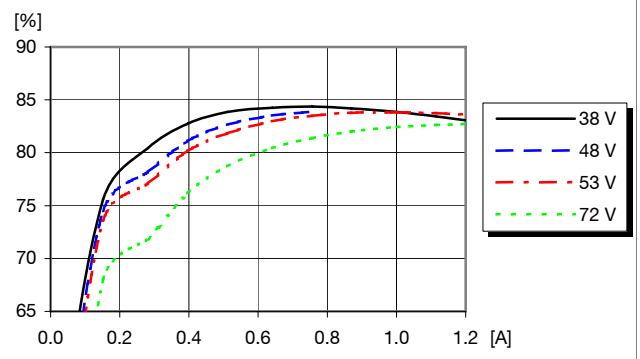
V_{Oi}	Output voltage initial setting and accuracy	T_{ref} = +25°C, V_I = 53 V, I_O = 0.8 A	5.02	5.05	5.08	V
	Output adjust range		4.30		5.80	V
V_O	Output voltage tolerance band	10-100% of max I_O	4.85		5.25	V
	Idling voltage	I_O = 0 A	5.2	5.5	6.0	V
	Line regulation	max I_O		10	20	mV
	Load regulation	V_I = 53 V, 10-100% of max I_O		120	210	mV
V_{tr}	Load transient voltage deviation	V_I = 53 V, Load step 25-75-25 % of max I_O, di/dt = 1 A/µs, see Note 1		-225 +140		mV
t_{tr}	Load transient recovery time			0.15		ms
t_r	Ramp-up time (from 10–90 % of V_{Oi})	10-100% of max I_O	0.5	1.0	2.0	ms
t_s	Start-up time (from V_I connection to 90% of V_{Oi})		1.7	3.0	7.0	ms
I_O	Output current		0		1.2	A
I_{lim}	Current limit threshold	V_O = 4.0 V, T_{ref} < max T_{ref}	1.4	1.7	2.1	A
I_{sc}	Short circuit current	T_{ref} = 25ºC		1.8	2.6	A
V_{Oac}	Output ripple & noise	See ripple & noise section, max I_O, V_{Oi}		5	50	mVp-p

Note 1: Output filter according to Ripple & Noise section

5.05 V/1.2 A Typical Characteristics

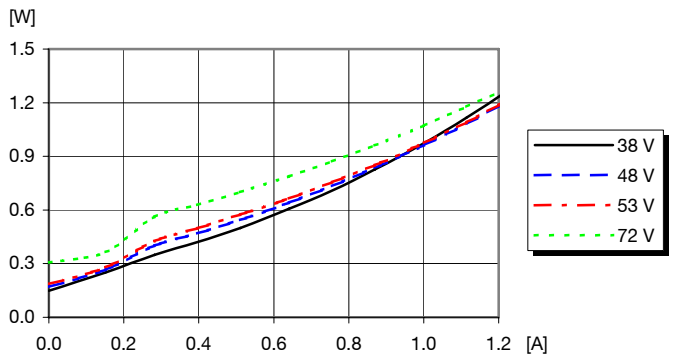
PKR 4611 SI

Efficiency



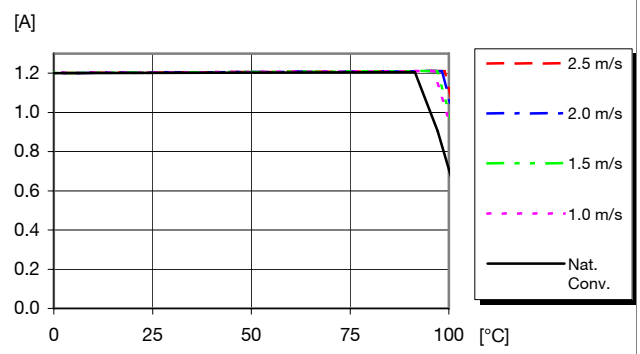
Efficiency vs. load current and input voltage at T_{ref} = +25°C.

Power Dissipation



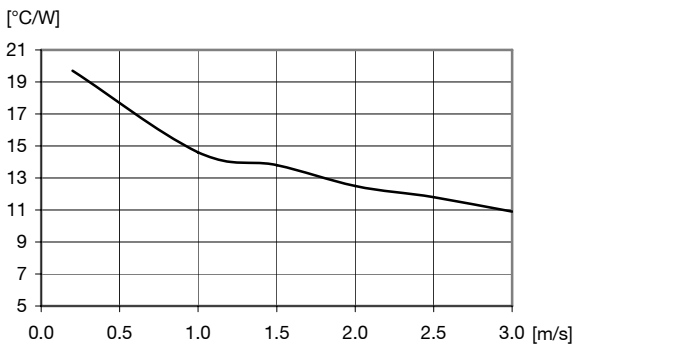
Dissipated power vs. load current and input voltage at T_{ref} = +25°C.

Output Current Derating



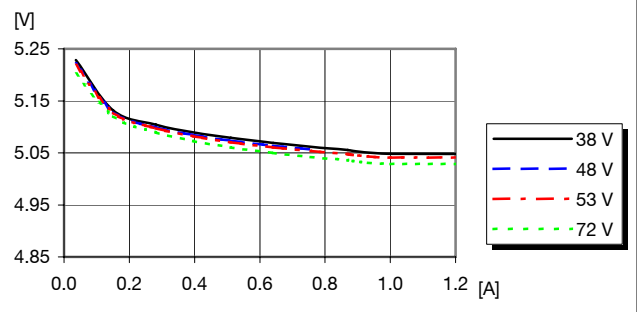
Available load current vs. ambient air temperature and airflow at
V_I = 53 V. See Thermal Consideration section.

Thermal Resistance



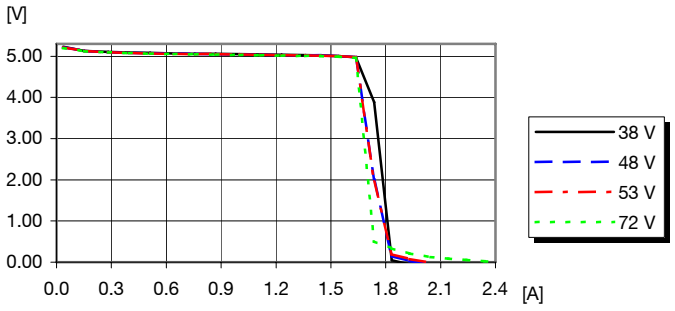
Thermal resistance vs. airspeed measured at the converter. Tested in wind tunnel with airflow and test conditions as per the Thermal consideration section.

Output Characteristics



Output voltage vs. load current at T_{ref} = +25°C.

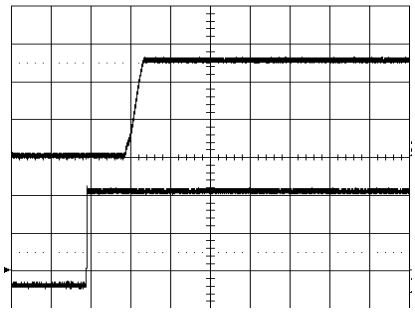
Current Limit Characteristics



Output voltage vs. load current at I_O > max I_O, T_{ref} = +25°C.

5.05 V/1.2 A Typical Characteristics PKR 4611 SI

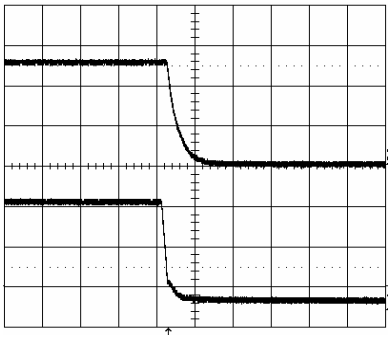
Start-up



Start-up enabled by connecting V_I at: T_{ref} = +25°C, I_O = 1.2 A resistive load, V_I = 53 V.

Top trace: output voltage (1 V/div.). Bottom trace: input voltage (20 V/div.). Time scale: 2 ms/div.

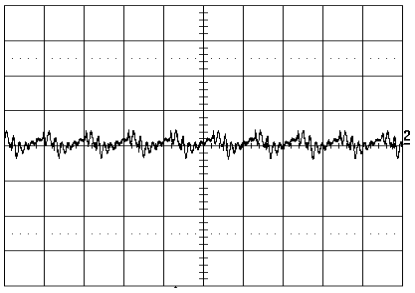
Shut-down



Shut-down enabled by disconnecting V_I at: T_{ref} = +25°C, I_O = 1.2 A resistive load, V_I = 53 V.

Top trace: output voltage (1 V/div.). Bottom trace: input voltage (20 V/div.). Time scale: 2 ms/div.

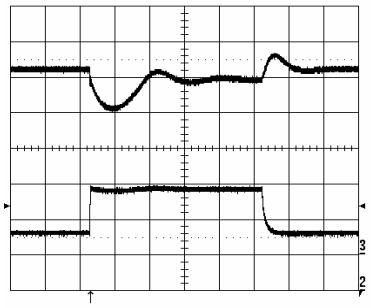
Output Ripple & Noise



Output voltage ripple (10mV/div.) at: T_{ref} = +25°C, I_O = 1.2 A resistive load, V_I = 53 V. Time scale: 2 µs/div.

See the filter in the Output ripple and noise section (EMC Specification).

Output Load Transient Response



Output voltage response to load current step-change (0.3-0.9-0.3 A) at: T_{ref} =+25°C, V_I = 53 V.

Top trace: output voltage (200mV/div.). Bottom trace: load current (0.5 A/div.). Time scale: 0.1 ms/div.

Output Voltage Adjust (see operating information)

Passive trim
The resistor value for an adjusted output voltage is calculated by using the following equations:

Output Voltage Adjust Upwards, Increase:
$R_{ou} = 0.495 \times (5.87 - V_O)/(V_O - 5.05)\ k\Omega$

E.g. Increase 4% =>V_{out} = 5.25 Vdc
$0.495 \times (5.87 - 5.25)/(5.25 - 5.05) = 1.5\ k\Omega$

Output Voltage Adjust Downwards, Decrease:
$R_{od} = 1.986 \times (5.05 - V_O)/(V_O - 4.12)\ k\Omega$

E.g. Decrease 2% =>V_{out} = 4.95 Vdc
$1.986 \times (5.05 - 4.95)/(4.95 - 4.12) = 0.239\ k\Omega$

12 V/0.6 A Electrical Specification — PKR 4713 SI

T_{ref} = -30 to +85ºC, V_I = 38 to 72 V, pin 8 connected to pin 9 unless otherwise specified under Conditions.
Typical values given at: T_{ref} = +25°C, V_I= 53 V, max I_O , unless otherwise specified under Conditions.

Characteristics		Conditions	min	typ	max	Unit
V_I	Input voltage range		38		72	V
V_{Ioff}	Turn-off input voltage	Decreasing input voltage	30	33.4	36	V
V_{Ion}	Turn-on input voltage	Increasing input voltage	32	35.1	38	V
C_I	Internal input capacitance			2		µF
P_O	Output power	Output voltage initial setting	0		7	W
SVR	Supply voltage rejection (ac)	f = 100 Hz sine wave, 1 Vp-p		60		dB
η	Efficiency	50 % of max I_O		86		%
		max I_O		85		
		50 % of max I_O, V_I = 48 V		86		
		max I_O, V_I = 48 V		85		
P_d	Power Dissipation	max I_O		1.25	1.75	W
P_{li}	Input idling power	I_O= 0 A, V_I = 53 V		87		mW
P_{RC}	Input standby power	V_I = 53V (turned off with RC)		33		mW
f_s	Switching frequency	50-100% of max I_O	412	485	558	kHz

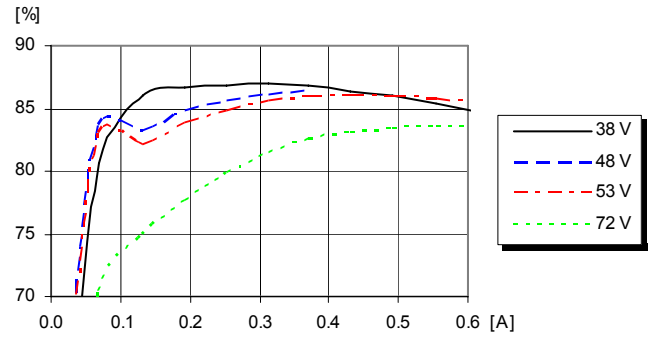
V_{Oi}	Output voltage initial setting and accuracy	T_{ref} = +25°C, V_I = 53 V, 50% of max I_O	11.83	12.0	12.18	V
	Output adjust range		10.2		13.8 (15)	V
V_O	Output voltage tolerance band	10-100% of max I_O	11.5		12.5	V
	Idling voltage	I_O = 0 A	13.3		18.6	V
	Line regulation	max I_O		60	140	mV
	Load regulation	V_I = 53V, 10-100% of max I_O		340	650	mV
V_{tr}	Load transient voltage deviation	V_I = 53 V, Load step 25-75-25 % of max I_O, di/dt = 5 A/µs, see Note 1		+100 -300		mV
t_{tr}	Load transient recovery time			0.30		ms
t_r	Ramp-up time (from 10–90 % of V_{Oi})	10-100% of max I_O	0.6	0.8	1.0	ms
t_s	Start-up time (from V_I connection to 90% of V_{Oi})		1	3	12	ms
I_O	Output current		0		0.6	A
I_{lim}	Current limit threshold	V_O = 10 V, T_{ref} < max T_{ref}	0.65	0.90	1.2	A
I_{sc}	Short circuit current	T_{ref} = 25ºC		1.2	1.9	A
V_{Oac}	Output ripple & noise	See ripple & noise section, max I_O, V_{Oi}		10	50	mVp-p

Note 1: Output filter according to Ripple & Noise section

12 V/0.6 A Typical Characteristics

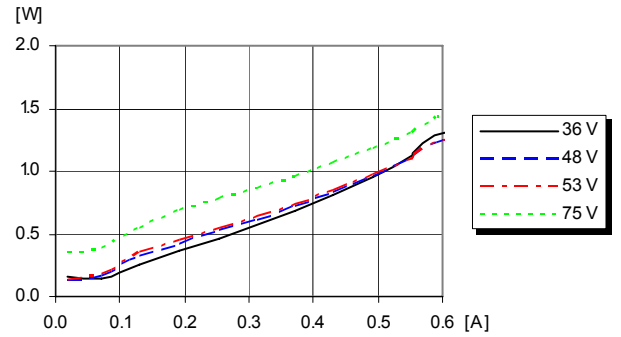
PKR 4713 SI

Efficiency



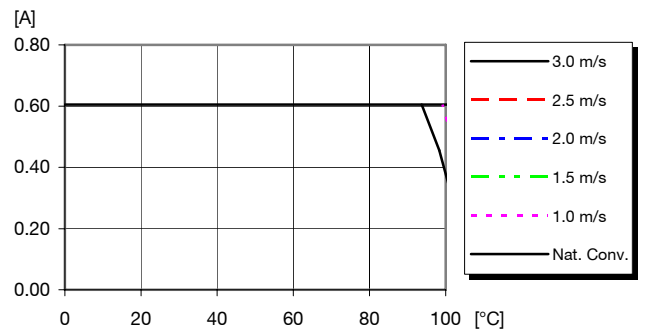
Efficiency vs. load current and input voltage at T_{ref} = +25°C

Power Dissipation



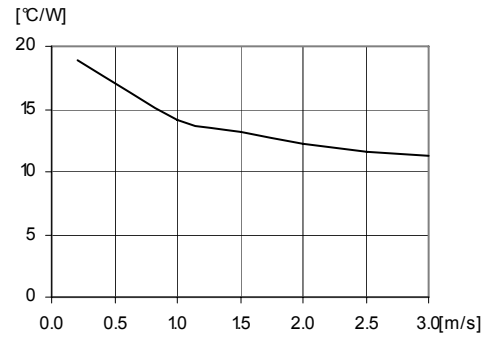
Dissipated power vs. load current and input voltage at T_{ref} = +25°C

Output Current Derating



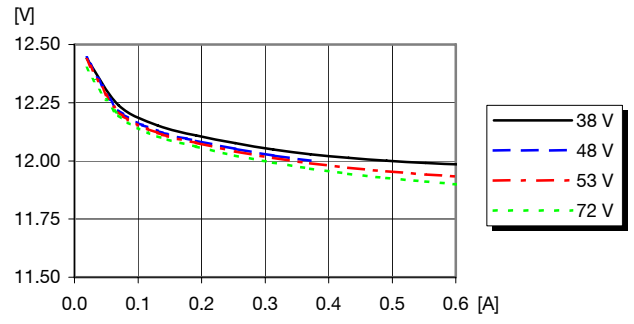
Available load current vs. ambient air temperature and airflow at V_I = 53 V. See Thermal Consideration section.

Thermal Resistance



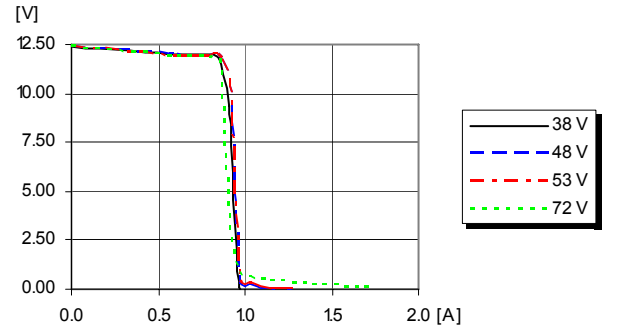
Thermal resistance vs. airspeed measured at the converter. Tested in wind tunnel with airflow and test conditions as per the Thermal consideration section.

Output Characteristics



Output voltage vs. load current at T_{ref} = +25°C

Current Limit Characteristics

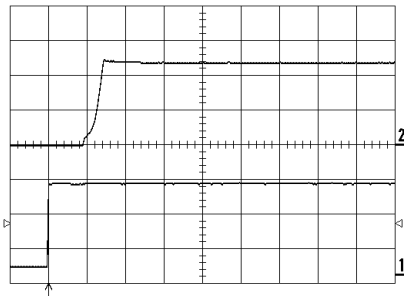


Output voltage vs. load current at I_O > max I_O, T_{ref} = +25°C

12 V/0.6 A Typical Characteristics

PKR 4713 SI

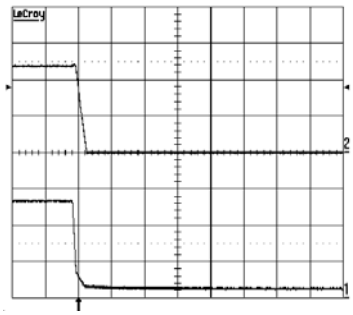
Start-up



Start-up enabled by connecting V_I at:
T_{ref} = +25°C, I_O = 0.6A resistive load,
V_I = 53V

Top trace: output voltage (5 V/div.).
Bottom trace: input voltage (20 V/div.).
Time scale: 2 ms/div.

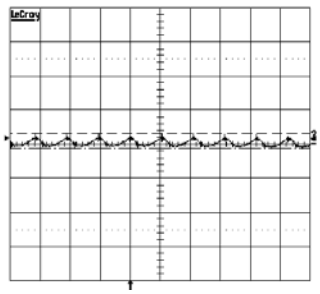
Shut-down



Shut-down enabled by disconnecting V_I at:
T_{ref} = +25°C, I_O = 0.6A resistive load,
V_I = 53 V

Top trace: output voltage (5 V/div.).
Bottom trace: input voltage (20 V/div.).
Time scale: 2 ms/div.

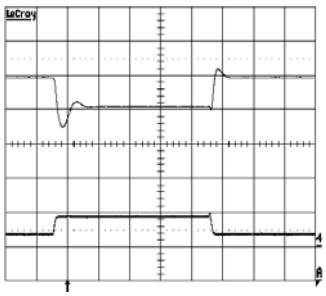
Output Ripple & Noise



Output voltage ripple (10mV/div.) at:
T_{ref} = +25°C, I_O = 0.6 A resistive load,
V_I = 53 VTime scale: 2 µs/div.

See the filter in the Output ripple and noise section (EMC Specification).

Output Load Transient Response



Output voltage response to load current step-change (0.15-0.45-0.15 A) at:
T_{ref} =+25°C, V_I = 53 V

Top trace: output voltage (200mV/div.).
Bottom trace: load current (1 A/div.).
Time scale: 0.2 ms/div.

Output Voltage Adjust (see operating information)

Passive trim
The resistor value for an adjusted output voltage is calculated by using the following equations:

To adjust the output voltage upwards, a resistor is connected between pins 8 and 18. Pins 8 and 9 have to be shorted. The output voltage increases when the resistance decreases. The resistance value is given by the equation:
Rou= 0.566*(15-Vo)/(Vo-12), (kOhm); Vo is the desired output voltage. Over 13.8V output voltage the input voltage range is limited to 38…65 V.

To adjust the output voltage downwards, a resistor is connected between pins 8 and 9. The output voltage decreases when the connected resistance value increases. The resistance value is given by the equation:
Rod= 2.284*(12-V_o)/(V_o-9.52), (kOhm), V_o is the desired output voltage.

Dual ±12 V/0.25 A, Electrical Specification — PKR 4621 SI

T_{ref} = -30 to +85ºC, V_I = 38 to 72 V, pin 8 connected to pin 9 unless otherwise specified under Conditions.
Typical values given at: T_{ref} = +25°C, V_I= 53 V, max I_O, unless otherwise specified under Conditions.

Characteristics		Conditions	min	typ	max	Unit
V_I	Input voltage range		38		72	V
V_{Ioff}	Turn-off input voltage	Decreasing input voltage	30	33.4	36	V
V_{Ion}	Turn-on input voltage	Increasing input voltage	32	35.7	38	V
C_I	Internal input capacitance			2		µF
P_O	Output power	Output voltage initial setting	0		6	W
SVR	Supply voltage rejection (ac)	f = 100 Hz sine wave, 1 Vp-p		66		dB
η	Efficiency	I_{O1} = 0.12 A, I_{O2} = 0.12 A		86		%
		I_{O1} = 0.12 A, I_{O2} = 0.12 A		86		
		I_{O1} = 0.12 A, I_{O2} = 0.12 A, V_I = 48 V		86		
		I_{O1} = 0.25 A, I_{O2} = 0.25 A, V_I = 48 V		86		
P_d	Power Dissipation	I_{O1} = I_{O2} = 0.25 A		1.0	1.3	W
P_{li}	Input idling power	I_O= 0 A, V_I =53 V		100		mW
P_{RC}	Input standby power	V_I = 53 V (turned off with RC)		32		mW
f_s	Switching frequency	I_{O1} = I_{O2} = 0.12…0.25 A	412	485	558	kHz

			Output 1			Output 2			
			min	typ	max	min	typ	max	
V_{Oi}	Output voltage initial setting and accuracy	T_{ref} = +25°C, V_I = 53 V, I_{O1} = I_{O2} = 0.15 A, see Note 2, 3	11.83	12.0	12.18		12.0		V
	Output adjust range		10.2		13.8	10.2		13.8	V
V_O	Output voltage tolerance band	10-100% of max I_O	11.5		12.5	11.4		12.6	V
	Idling voltage	I_O = 0	13		20	13		20	V
	Line regulation	I_{O1} = I_{O2} = 0.25 A		23	90		22	80	mV
	Load regulation output 1	V_I = 53 V, I_{O1} = 0.025…0.25 A, I_{O2} = 0.25 A		340	470				mV
	Load regulation output 2	V_I = 53 V, I_{O2} = 0.025…0.25 A, I_{O1} = 0.25 A					340	455	mV
V_{tr}	Load transient voltage deviation	V_I = 53 V, load step I_{O1} = 0.1-0.2-0.1 A, I_{O2}= 0.25 A, di/dt = 1 A/µs, see Note 1		-210 +56			-200 +56		mV
t_{tr}	Load transient recovery time			0.2			0.2		ms
t_r	Ramp-up time (from 10-90% of V_{Oi})	I_{O1} = I_{O2} = 0.025…0.25 A	0.4	1.0	2.9	0.4	1.0	2.9	ms
t_s	Start-up time (from V_I connection to 90% of V_{Oi})		1.5	3.2	7	1.5	3.2	7	ms
I_O	Output current		0		0.5	0		0.5	A
I_{lim}	Current limit threshold	V_O = 10 V, T_{ref} < max T_{ref}	0.35	0.76	1.1	0.35	0.76	1.1	A
I_{sc}	Short circuit current	T_{ref} = 25ºC		1.1	1.7		1.1	1.7	A
V_{Oac}	Output ripple & noise	See ripple & noise section, max I_O, V_{Oi}		6	50		6	50	mVp-p

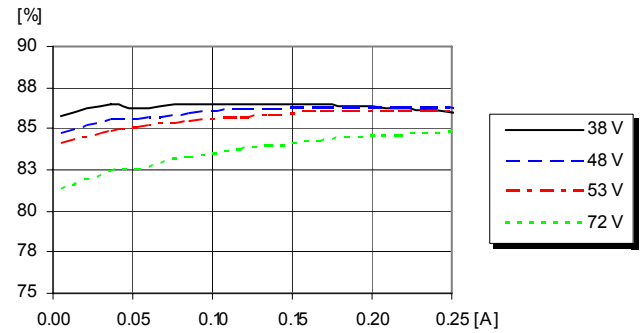
Note 1: Output filter according to Ripple & Noise section

Note 2: Output voltage on Output 2 is negative (-12V)
Note 3: Can be adjusted to 14.4V. Over 13.8V output voltage, the input voltage range is limited to 38…65V

Dual ±12 V/0.25 A, Typical Characteristics

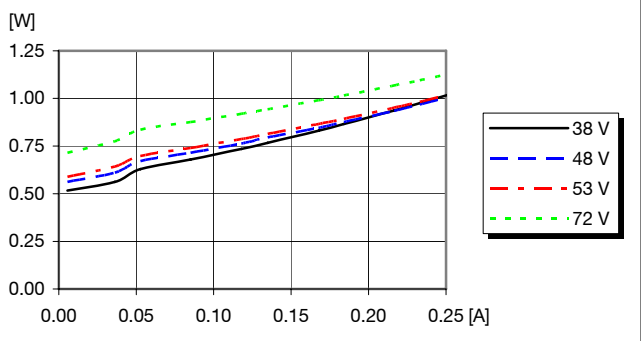
PKR 4621 SI

Efficiency



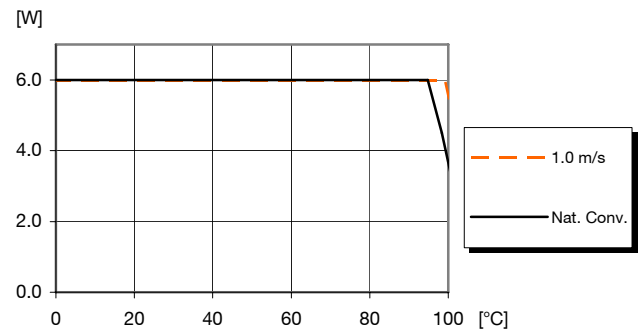
Output 1. Efficiency vs. load current and input voltage at T_{ref} = +25°C, I_{02} = 0.25A.

Power Dissipation



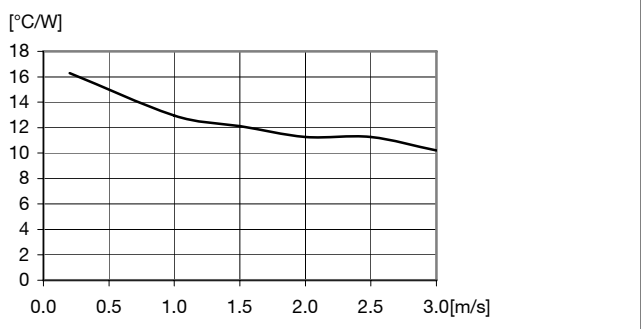
Output 1. Dissipated power vs. load current and input voltage at T_{ref} = +25°C, I_{02} = 0.25A.

Output Power Derating



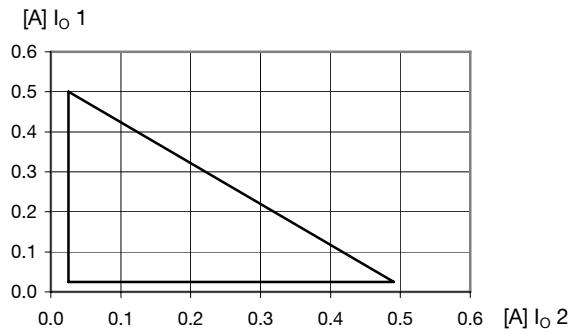
Available load current vs. ambient air temperature and airflow at V_I = 53 V. See Thermal Consideration section.

Thermal Resistance



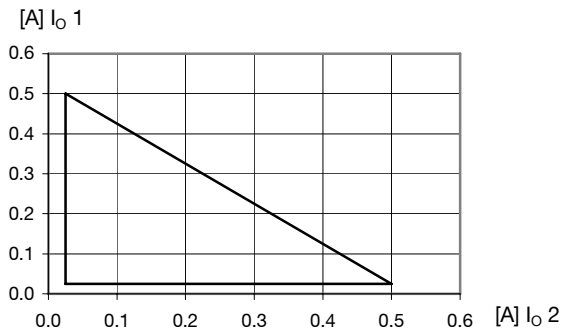
Thermal resistance vs. airspeed measured at the converter. Tested in wind tunnel with airflow and test conditions as per the Thermal consideration section.

Cross regulation Output 1 (+12V)



Operation area for ±4% tolerance at T_{ref} = +25°C.

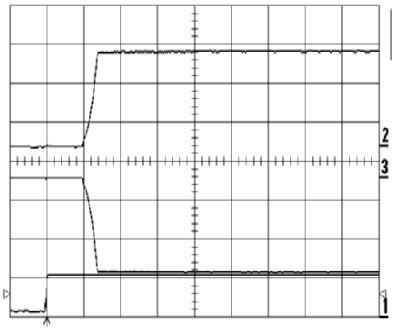
Cross regulation Output 2 (-12V)



Operation area for ±5% tolerance at T_{ref} = +25°C.

Dual ±12 V/0.25 A, Typical Characteristics — PKR 4621 SI

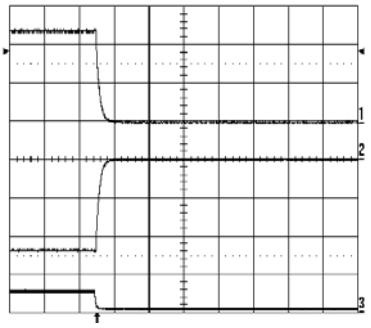
Start-up



Start-up enabled by connecting V_I at:
T_{ref} = +25°C, I_{01} = I_{02} = 0.25 A resistive load,
V_I = 53 V.

Top trace: Output 1 (5 V/div.).
Mid trace: Output 2 (5 V/div.).
Bottom trace: input voltage (50 V/div.).
Time scale: 2 ms/div.

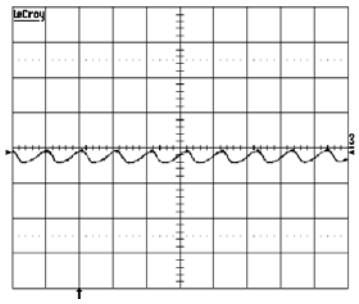
Shut-down



Shut-down enabled by disconnecting V_I at:
T_{ref} = +25°C, I_{01} = I_{02} = 0.25 A resistive load,
V_I = 53 V.

Top trace: Output 1 (5 V/div.).
Mid trace: Output 2 (5 V/div.).
Bottom trace: input voltage (100 V/div.).
Time scale: 5 ms/div.

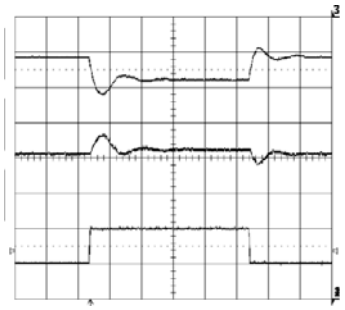
Output Ripple & Noise



Output voltage ripple (10mV/div.) at:
T_{ref} = +25°C, I_{01} = I_{02} = 0.25 A resistive load,
V_I = 53 V. Time scale: 2 µs/div.

See the filter in the Output ripple and noise section (EMC Specification).

Output Load Transient Response



Output voltage response to load current step-change Output 1(0.1-0.2-0.1A) at:
T_{ref} =+25°C, I_{02} = 0.25A , V_I = 53 V.

Top trace: Output 1 (200mV/div.).
Mid trace: Output 2 (200mV/div.).
Bottom trace: Load current Output 1 (0.1 A/div.).
Time scale: 0.1 ms/div.

Output Voltage Adjust (see operating information)

Passive trim
The resistor value for an adjusted output voltage is calculated by using the following equations:

Output Voltage Adjust Upwards, Increase:
$R_{ou} = 0.566 \times (14.4 - V_O)/(V_O - 12)$ kΩ

E.g. Increase 4% =>V_{out} = 12.48 Vdc
0.566 ×(14.4 – 12.48)/(12.48 – 12) = 2.26 kΩ

Output Voltage Adjust Downwards, Decrease:
$R_{od} = 2.284 \times (12 - V_O)/(V_O - 9.52)$ kΩ

E.g. Decrease 2% =>V_{out} = 11.76 Vdc
2.284 ×(12 –11.76)/(11.76 – 9.52) = 0.245 kΩ

Dual ±5 V/0.6 A, Electrical Specification — PKR 4622 SI

T_{ref} = -30 to +85ºC, V_I = 38 to 72 V, pin 8 connected to pin 9 unless otherwise specified under Conditions.
Typical values given at: T_{ref} = +25°C, V_I= 53 V, max I_O, unless otherwise specified under Conditions.

Characteristics		Conditions	min	typ	max	Unit
V_I	Input voltage range		38		72	V
V_{Ioff}	Turn-off input voltage	Decreasing input voltage	30	32.0	36	V
V_{Ion}	Turn-on input voltage	Increasing input voltage	32	35.3	38	V
C_I	Internal input capacitance			2		µF
P_O	Output power	Output voltage initial setting	0		6	W
SVR	Supply voltage rejection (ac)	f = 100 Hz sine wave, 1 Vp-p		64		dB
η	Efficiency	I_{O1} = 0.3 A, I_{O2} = 0.3 A		84		%
		I_{O1} = 0.6 A, I_{O2} = 0.6 A		84		
		I_{O1} = 0.3 A, I_{O2} = 0.3 A, V_I = 48 V		84		
		I_{O1} = 0.6 A, I_{O2} = 0.6 A, V_I = 48 V		84		
P_d	Power Dissipation	I_{O1} = 0.6 A, I_{O2} = 0.6 A		1.2	1.5	W
P_{li}	Input idling power	I_O= 0 A, V_I =53 V		150		mW
P_{RC}	Input standby power	V_I = 53 V (turned off with RC)		30		mW
f_s	Switching frequency	I_{O1} = I_{O2} = 0.3…0.6 A	412	485	558	kHz

			Output 1			Output 2			
			min	typ	max	min	typ	max	
V_{Oi}	Output voltage initial setting and accuracy	T_{ref} = +25°C, V_I = 53 V, I_{O1} = 0.3 A, I_{O2} = 0.3 A, see Note 2	5.02	5.05	5.08		5.05		V
	Output adjust range		4.30		5.80	4.30		5.80	V
V_O	Output voltage tolerance band	10-100% of max I_O	4.85		5.25	4.80		5.30	V
	Idling voltage	I_O = 0 A	5.1		6.3	5.1		6.3	V
	Line regulation	I_{O1} = 0.6 A, I_{O2} = 0.6 A		18	52		23	59	mV
	Load regulation output 1	V_I = 53 V, I_{O1} = 0.06…0.6 A, I_{O2} = 0.6 A		190	300				mV
	Load regulation output 2	V_I = 53 V, I_{O2} = 0.06…0.6 A, I_{O1} = 0.6 A					180	280	mV
V_{tr}	Load transient voltage deviation	V_I = 53 V, load step I_{O1} = 0.3-0.45-0.3 A, I_{O2}= 0.6 A, di/dt = 1 A/µs, see Note 1		±80			±80		mV
t_{tr}	Load transient recovery time			0.15			0.15		ms
t_r	Ramp-up time (from 10-90% of V_{Oi})	I_{O1} = I_{O2} = 0.025…0.25 A	0.1	0.7	1.6	0.1	0.7	1.6	ms
t_s	Start-up time (from V_I connection to 90% of V_{Oi})		1.5	2.8	7	1.5	2.8	7	ms
I_O	Output current		0		1.0	0		1.0	A
I_{lim}	Current limit threshold	V_O = 4.0 V, T_{ref} < max T_{ref}	1.1	1.7	2.4	1.1	1.7	2.4	A
I_{sc}	Short circuit current	T_{ref} = 25ºC		1.9	2.9		1.9	2.9	A
V_{Oac}	Output ripple & noise	See ripple & noise section, max I_O, V_{Oi}		11	50		13	50	mVp-p

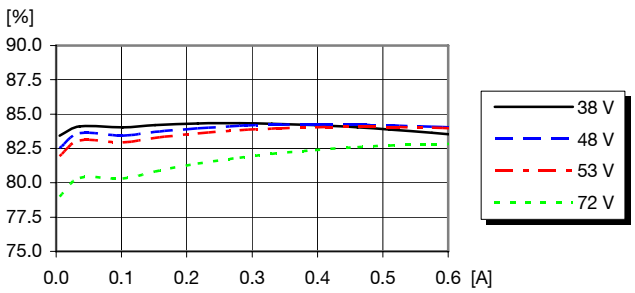
Note 1: Output filter according to Ripple & Noise section

Note 2: Output voltage on Output 2 is negative (-5V)

Dual ±5 V/0.6 A, Typical Characteristics

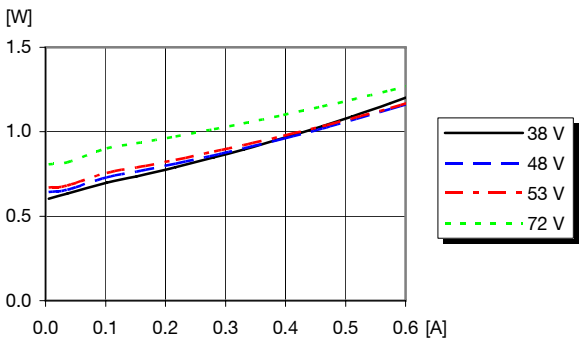
PKR 4622 SI

Efficiency



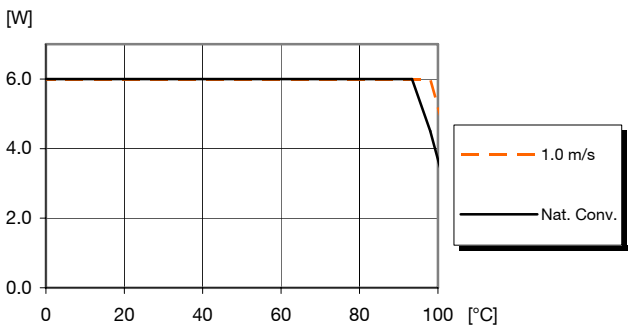
Output 1. Efficiency vs. load current and input voltage at T_{ref} = +25°C, I_{02} = 0.6A.

Power Dissipation



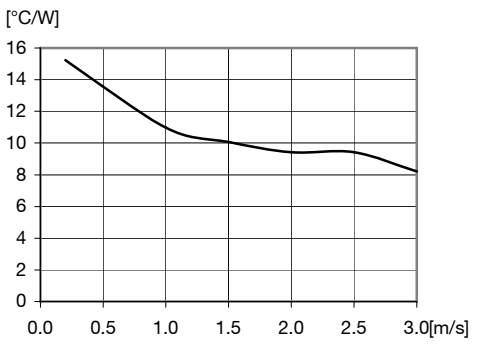
Output 1. Dissipated power vs. load current and input voltage at T_{ref} = +25°C, I_{02} = 0.6A.

Output Power Derating



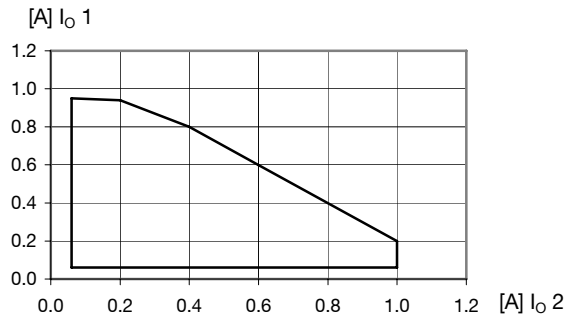
Available load current vs. ambient air temperature and airflow at V_I = 53 V. See Thermal Consideration section.

Thermal Resistance



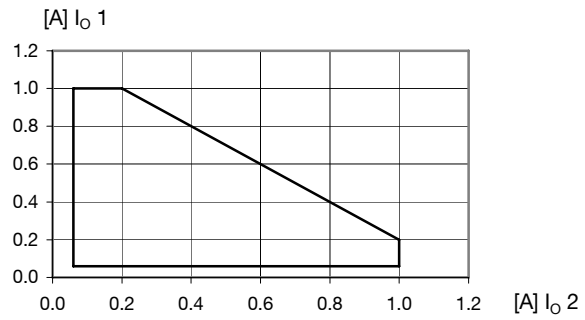
Thermal resistance vs. airspeed measured at the converter. Tested in wind tunnel with airflow and test conditions as per the Thermal consideration section.

Cross regulation Output 1 (+5V)



Operation area for ±4% tolerance at T_{ref} = +25°C.

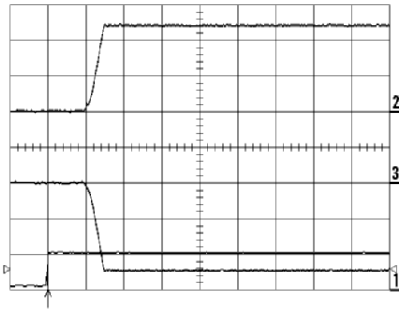
Cross regulation Output 2 (-5V)



Operation area for ±5% tolerance at T_{ref} = +25°C.

Dual ±5 V/0.6 A, Typical Characteristics — PKR 4622 SI

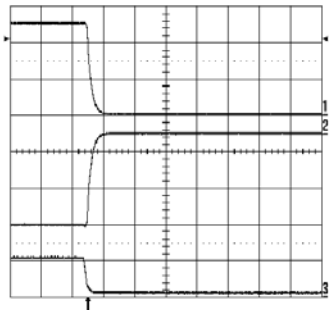
Start-up



Start-up enabled by connecting V_I at: T_{ref} = +25°C, I_{01} = I_{02} = 0.6 A resistive load, V_I = 53 V.

Top trace: Output 1 (2 V/div.).
Mid trace: Output 2 (2 V/div.).
Bottom trace: Input voltage (50 V/div.).
Time scale: 2 ms/div.

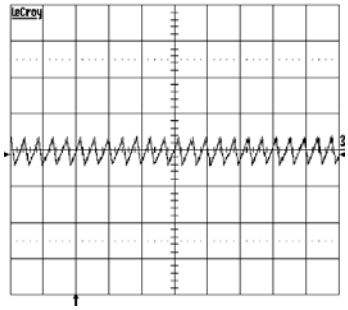
Shut-down



Shut-down enabled by disconnecting V_I at: T_{ref} = +25°C, I_{01} = I_{02} = 0.6 A resistive load, V_I = 53 V.

Top trace: Output 1 (2 V/div.).
Mid trace: Output 2 (2 V/div.).
Bottom trace: Input voltage (50 V/div.).
Time scale: 2 ms/div.

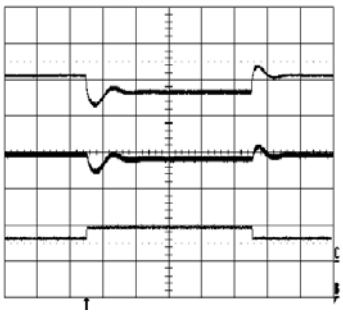
Output Ripple & Noise



Output voltage ripple Output 1 (10mV/div.) at: T_{ref} = +25°C, I_{01} = I_{02} = 0.6 A resistive load, V_I = 53 V. Time scale: 5 µs/div.

See the filter in the Output ripple and noise section (EMC Specification).

Output Load Transient Response



Output voltage response to load current step-change Output 1(0.3-0.45-0.3A) at: T_{ref} =+25°C, I_{02} = 0.6 A, V_I = 53 V.

Top trace: Output 1 (100mV/div.).
Mid trace: Output 2 (100mV/div.).
Bottom trace: Load current Output 1 (0.5 A/div.).
Time scale: 0.1 ms/div.

Output Voltage Adjust (see operating information)

Passive trim
The resistor value for an adjusted output voltage is calculated by using the following equations:

Output Voltage Adjust Upwards, Increase:
$R_{ou} = 0.495 \times (5.87 - V_O)/(V_O - 5.05)$ kΩ

E.g. Increase 4% =>V_{out} = 5.25 Vdc
0.495 ×(5.87 – 5.25)/(5.25 – 5.05) = 1.53 kΩ

Output Voltage Adjust Downwards, Decrease:
$R_{od} = 1.986 \times (5.05 - V_O)/(V_O - 4.12)$ kΩ

E.g. Decrease 2% =>V_{out} = 4.95 Vdc
1.986 ×(5.05 –4.95)/(4.95 – 4.12) = 0.239 kΩ

Dual 5 V/0.6 A, 3.3 V/0.9 A, Electrical Specification — PKR 4628 SI

T_{ref} = 38 to +85ºC, V_I = 38 to 72 V, unless otherwise specified under Conditions.
Typical values given at: T_{ref} = +25°C, V_I= 53 V, max I_O unless otherwise specified under Conditions.

Characteristics		Conditions	min	typ	max	Unit
V_I	Input voltage range		38		72	V
V_{Ioff}	Turn-off input voltage	Decreasing input voltage	30	33.4	36	V
V_{Ion}	Turn-on input voltage	Increasing input voltage	32	35	38	V
C_I	Internal input capacitance			2		µF
P_O	Output power	Output voltage initial setting	0		6	W
SVR	Supply voltage rejection (ac)	f = 100 Hz sine wave, 1 Vp-p		60		dB
η	Efficiency	I_{O1} = 0.3 A, I_{O2} = 0.45 A		82		%
		I_{O1} = 0.6 A, I_{O2} = 0.9 A		83		
		I_{O1} = 0.3 A, I_{O2} = 0.45 A, V_I = 48 V		82		
		I_{O1} = 0.6 A, I_{O2} = 0.9 A, V_I = 48 V		83		
P_d	Power Dissipation	I_{O1} = 0.6 A, I_{O2} = 0.9 A		1.3	1.4	W
P_{li}	Input idling power	I_O= 0 A, V_I = 53 V		105		mW
P_{RC}	Input standby power	V_I = 53 V (turned off with RC)		30		mW
f_s	Switching frequency	I_{O1} = 0.3…0.6 A, I_{O2} = 0.45…0.9 A	412	485	558	kHz

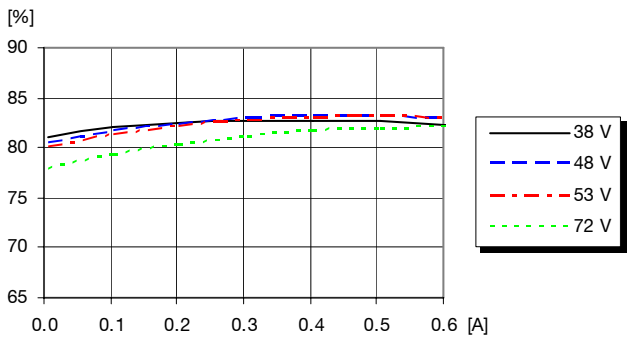
			Output 1			Output 2			
			min	typ	max	min	typ	max	
V_{Oi}	Output voltage initial setting and accuracy	T_{ref} = +25°C, V_I = 53 V, I_{O1} = 0.6 A, I_{O2} = 0.9 A		5.20		3.25	3.27	3.29	V
	Output adjust range		4.43		5.97	2.80		3.80	V
V_O	Output voltage tolerance band	10-100% of max I_O	4.94		5.46	3.17		3.42	V
	Idling voltage	I_O = 0 A	5.5		6.6	3.5		4.29	V
	Line regulation	I_{O1} = 0.6 A, I_{O2} = 0.9 A		28	100		10	25	mV
	Load regulation output 1	V_I = 53 V, I_{O1} = 0.06…0.6 A, I_{O2} = 0.9 A		148	290				mV
	Load regulation output 2	V_I = 53 V, I_{O2} = 0.09…0.9 A, I_{O1} = 0.6 A					145	240	mV
V_{tr}	Load transient voltage deviation	V_I = 53 V, Load step of I_{O1} = 0.6A 25–75–25%, I_{O2} = 0.9 A Di/dt = 1 A/µs, see Note 1		-300 +120			-120 +80		mV
t_{tr}	Load transient recovery time			150			150		µs
t_r	Ramp-up time (from 10-90% of V_{Oi})	I_{O1} = 0.6 A, I_{O2} = 0.9 A	0.2	1.1	7	0.1	1.0	6.2	ms
t_s	Start-up time (from V_I connection to 90% of V_{Oi})		1.6	3.4	8	1.6	3.4	8	ms
I_O	Output current		0	0.6	1	0	0.9	1	A
I_{lim}	Current limit threshold	V_{o1} = 4.0 V, V_{o2} = 2.5 V T_{ref} < max T_{ref}	1.5	1.6	2.2	1.5	1.6	2.5	A
I_{sc}	Short circuit current	T_{ref} = 25ºC,		2.0	2.8		2.3	3.1	A
V_{Oac}	Output ripple & noise	See ripple & noise section, max I_O, V_{Oi}		20	80		6	50	mVp-p

Note 1: Output filter according to Ripple & Noise section.

Dual 5 V/0.6 A, 3.3 V/0.9 A, Typical Characteristics

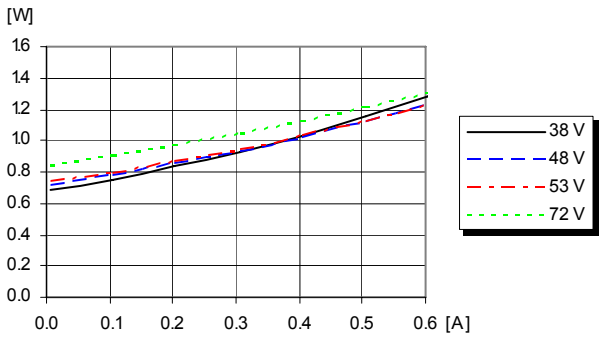
PKR 4628 SI

Efficiency



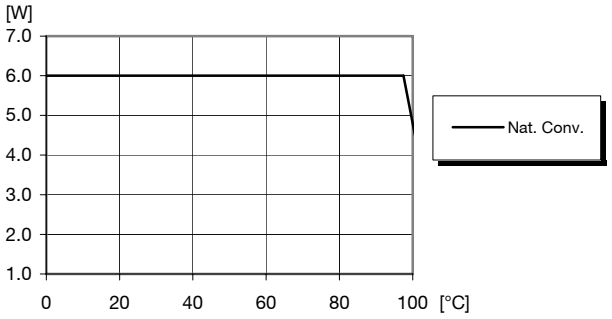
Output 1. Efficiency vs. load current and input voltage at T_{ref} = +25°C, I_{02} = 0.9 A.

Power Dissipation



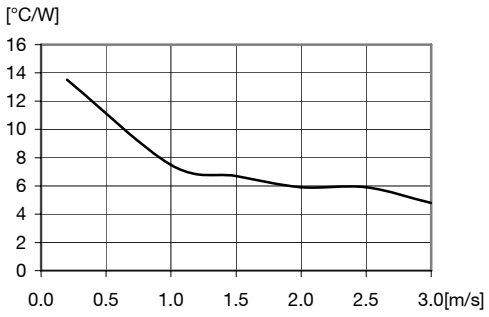
Output 1. Dissipated power vs. load current and input voltage at T_{ref} = +25°C, I_{02} = 0.9 A

Output Power Derating



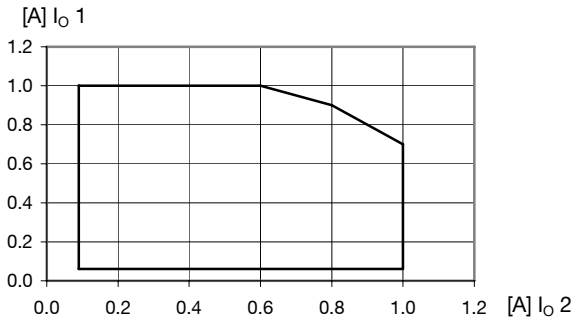
Output 1. Available load current vs. ambient air temperature and airflow at V_I = 53 V. See Thermal Consideration section.

Thermal Resistance



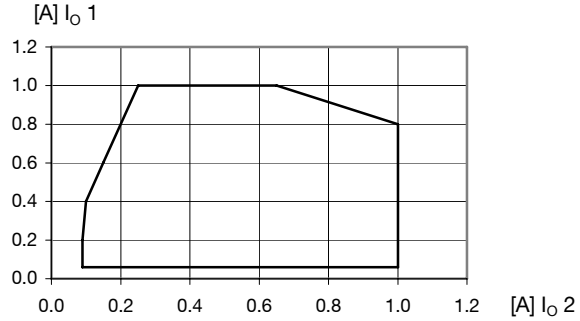
Thermal resistance vs. airspeed measured at the converter. Tested in wind tunnel with airflow and test conditions as per the Thermal consideration section.

Cross regulation output 1 (+5V)



Operation area for ±5% tolerance at T_{ref} = +25°C.

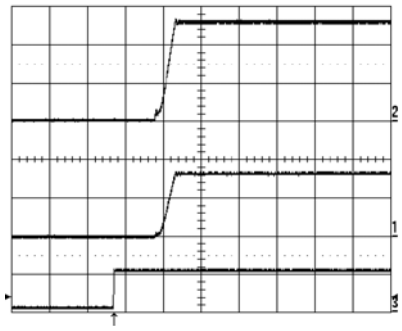
Cross regulation output 2 (+3.3V)



Operation area for ±4% tolerance at T_{ref} = +25°C.

Dual 5 V/0.6 A, 3.3 V/0.9 A, Typical Characteristics — PKR 4628 SI

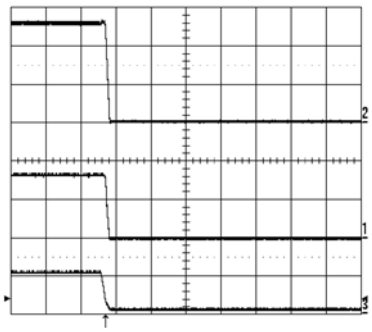
Start-up



Start-up enabled by connecting V_I at: T_{ref} = +25°C, I_{O1} = 0.6 A, I_{O2} = 0.9 A resistive load, V_I = 53 V

Top trace: Output 1 (2 V/div.).
Mid trace: Output 2 (2 V/div.).
Bottom trace: Input voltage (50 V/div.).
Time scale: 2 ms/div.

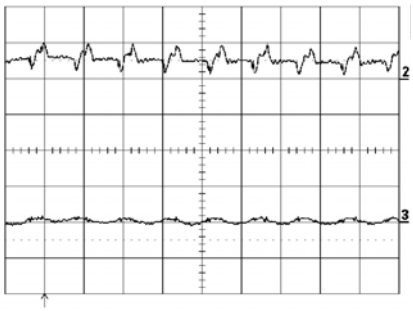
Shut-down



Shut-down enabled by disconnecting V_I at: T_{ref} = +25°C, I_{O1} = 0.6 A, I_{O2} = 0.9 A resistive load, V_I = 53 V.

Top trace: Output 1 (2 V/div.).
Mid trace: Output 2 (2 V/div.).
Bottom trace: Input voltage (50 V/div.).
Time scale: 2 ms/div.

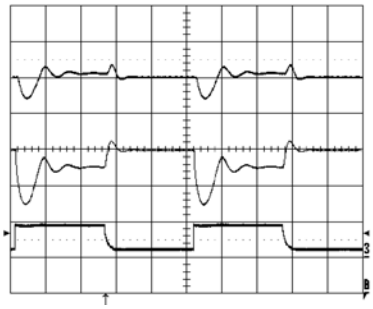
Output Ripple & Noise



Output voltage ripple (20mV/div.) at: T_{ref} = +25°C, I_{O1} = 0.6 A, I_{O2} = 0.9 A resistive load, V_I = 53 V. Time scale: 2 µs/div

Top trace: Output 1 (20 mV/div.).
Bottom trace: Output 2 (20 mV/div.).
See the filter in the Output ripple and noise section (EMC Specification).

Output Load Transient Response



Output voltage response to load current step-change (0.15-0.45-0.15 A) at: T_{ref}=+25°C, V_I = 53 V. I_{O2} = 0.9A resistive load

Top trace: Output 2 (200mV/div.).
Mid trace: Output 1 (200mV/div.).
Bottom trace: load current Output 1 (0.5 A/div.).
Time scale: 0.1 ms/div.

Output Voltage Adjust (see operating information)

Passive trim
The resistor value for an adjusted output voltage is calculated by using the following equations:

Output Voltage Adjust Upwards, Increase:
$R_{ou} = 0.495 \times (3.93 - V_O)/(V_O - 3.27)k\Omega$

E.g. Increase 4% =>V_o = 3.40 Vdc
0.495 × (3.93 – 3.4)/(3.4 –3.27) = 2.02 kΩ

Output Voltage Adjust Downwards, Decrease:
$R_{od} = 1.986 \times (3.27 - V_O)/(V_O - 2.59)k\Omega$

E.g. Decrease 2% =>V_o = 3.20 Vdc
1.986 × (3.27 – 3.20)/(3.20 – 2.59) = *0.228 kΩ*

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Technical Specification

PKR 4000A SI series DC/DC converters, Input 36-75 V, Output 3 A/15 W	EN/LZT 146 300 R5B April 2006

Key Features

- Industry standard MacroDens™ footprint 47.8 x 28.1 x max height 8.0 mm (1.88 x 1.11 x max height 0.32 in.)
- High efficiency, typ. 84 % at 5.0 Vout full load
- 1500 Vdc input to output isolation
- Meets isolation requirements equivalent to basic insulation according to IEC/EN/UL 60950
- More than 6.0 million hours predicted MTBF at 40°C ambient temperature

General Characteristics

- Suited for narrow board pitch applications (15 mm/0.6 in)
- Input under voltage protection
- Over temperature protection
- Output short-circuit protection
- Over current protection
- Soft start
- Remote control
- Output voltage adjust function
- Highly automated manufacturing to ensure highest quality
- ISO 9001/14001 certified supplier

Safety Approvals

Design for Environment

Meets requirements in high-temperature lead-free soldering processes.

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Safety Specification 3
Absolute Maximum Ratings 4

Product Program	Ordering No.	
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5.0 V /3.0 A Electrical Specification	PKR 4211A SI	8
7.0 V /2.1 A Electrical Specification	PKR 4117A SI	11

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Connections 18
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Technical Specification

PKR 4000A SI series DC/DC converters, Input 36-75 V, Output 3 A/15 W	EN/LZT 146 300 R5B April 2006

General Information

Ordering Information

See Contents for individual product ordering numbers.

Reliability

The Mean Time Between Failure (MTBF) is calculated at full output power and an operating ambient temperature (T_A) of +40°C, which is a typical condition in Information and Communication Technology (ICT) equipment. Different methods could be used to calculate the predicted MTBF and failure rate which may give different results. Ericsson Power Modules currently uses one method, Telcordia SR332.

Predicted MTBF for the series is:

- 6.0 million hours according to Telcordia SR332, issue 1, Black box technique.

Telcordia SR332 is a commonly used standard method intended for reliability calculations in ICT equipment. The parts count procedure used in this method was originally modelled on the methods from MIL-HDBK-217F, Reliability Predictions of Electronic Equipment. It assumes that no reliability data is available on the actual units and devices for which the predictions are to be made, i.e. all predictions are based on generic reliability parameters.

Compatibility with RoHS requirements

The products are compatible with the relevant clauses and requirements of the RoHS directive 2002/95/EC and have a maximum concentration value of 0.1% by weight in homogeneous materials for lead in other applications other than lead in solder, lead in high melting temperature type solder, lead in glass of electronics components, lead in electronic ceramic parts and lead as an alloying element in copper containing up to 4% lead by weight, mercury, hexavalent chromium, PBB and PBDE and of 0.01% by weight in homogeneous materials for cadmium.

Exemptions in the RoHS directive utilized in the products:

- Lead as an alloying element in copper alloy containing up to 4% lead by weight (used in connection pins made of Brass)
- Lead in high melting temperature type solder (used to solder the die in semiconductor packages)
- Lead in glass of electronics components and in electronic ceramic parts (e.g. fill material in chip resistors)
- Lead in solder for servers, storage and storage array systems, network infrastructure equipment for switching, signaling, transmission as well as network management for telecommunication
  (Note: the products are manufactured in lead-free soldering processes and the lead present in the solder is only located in the terminal plating finishes on some components)

Quality Statement

The products are designed and manufactured in an industrial environment where quality systems and methods like ISO 9000, 6σ (sigma), and SPC are intensively in use to boost the continuous improvements strategy. Infant mortality or early failures in the products are screened out and they are subjected to an ATE-based final test. Conservative design rules, design reviews and product qualifications, plus the high competence of an engaged work force, contribute to the high quality of our products.

Warranty

Warranty period and conditions are defined in Ericsson Power Modules General Terms and Conditions of Sale.

Limitation of Liability

Ericsson Power Modules does not make any other warranties, expressed or implied including any warranty of merchantability or fitness for a particular purpose (including, but not limited to, use in life support applications, where malfunctions of product can cause injury to a person’s health or life).

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Technical Specification

PKR 4000A SI series DC/DC converters, Input 36-75 V, Output 3 A/15 W	EN/LZT 146 300 R5B April 2006
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Safety Specification

General information
Ericsson Power Modules DC/DC converters and DC/DC regulators are designed in accordance with safety standards IEC/EN/UL60950, *Safety of Information Technology Equipment*.

IEC/EN/UL60950 contains requirements to prevent injury or damage due to the following hazards:

- Electrical shock
- Energy hazards
- Fire
- Mechanical and heat hazards
- Radiation hazards
- Chemical hazards

On-board DC-DC converters are defined as component power supplies. As components they cannot fully comply with the provisions of any Safety requirements without "Conditions of Acceptability". It is the responsibility of the installer to ensure that the final product housing these components complies with the requirements of all applicable Safety standards and Directives for the final product.

Component power supplies for general use should comply with the requirements in IEC60950, EN60950 and UL60950 *"Safety of information technology equipment".*

There are other more product related standards, e.g. IEEE802.3af "Ethernet LAN/MAN Data terminal equipment power", and ETS300132-2 "Power supply interface at the input to telecommunications equipment; part 2: DC", but all of these standards are based on IEC/EN/UL60950 with regards to safety.

Ericsson Power Modules DC/DC converters and DC/DC regulators are UL60950 recognized and certified in accordance with EN60950.

The flammability rating for all construction parts of the products meets requirements for V-0 class material according to IEC 60695-11-10.

The products should be installed in the end-use equipment, in accordance with the requirements of the ultimate application. Normally the output of the DC/DC converter is considered as SELV (Safety Extra Low Voltage) and the input source must be isolated by minimum Double or Reinforced Insulation from the primary circuit (AC mains) in accordance with IEC/EN/UL60950.

Isolated DC/DC converters
It is recommended that a slow blow fuse with a rating twice the maximum input current per selected product be used at the input of each DC/DC converter. If an input filter is used in the circuit the fuse should be placed in front of the input filter.
In the rare event of a component problem in the input filter or in the DC/DC converter that imposes a short circuit on the input source, this fuse will provide the following functions:

- Isolate the faulty DC/DC converter from the input power source so as not to affect the operation of other parts of the system.
- Protect the distribution wiring from excessive current and power loss thus preventing hazardous overheating.

The galvanic isolation is verified in an electric strength test. The test voltage (V_{iso}) between input and output is 1500 Vdc or 2250 Vdc for 60 seconds (refer to product specification).
Leakage current is less than 1 µA at nominal input voltage.

24 V DC systems
The input voltage to the DC/DC converter is SELV (Safety Extra Low Voltage) and the output remains SELV under normal and abnormal operating conditions.

48 and 60 V DC systems
If the input voltage to Ericsson Power Modules DC/DC converter is 75 Vdc or less, then the output remains SELV (Safety Extra Low Voltage) under normal and abnormal operating conditions.

Single fault testing in the input power supply circuit should be performed with the DC/DC converter connected to demonstrate that the input voltage does not exceed 75 Vdc.

If the input power source circuit is a DC power system, the source may be treated as a TNV2 circuit and testing has demonstrated compliance with SELV limits and isolation requirements equivalent to Basic Insulation in accordance with IEC/EN/UL60950.

Non-isolated DC/DC regulators
The input voltage to the DC/DC regulator is SELV (Safety Extra Low Voltage) and the output remains SELV under normal and abnormal operating conditions.

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Technical Specification

PKR 4000A SI series DC/DC converters, Input 36-75 V, Output 3 A/15 W	EN/LZT 146 300 R5B April 2006
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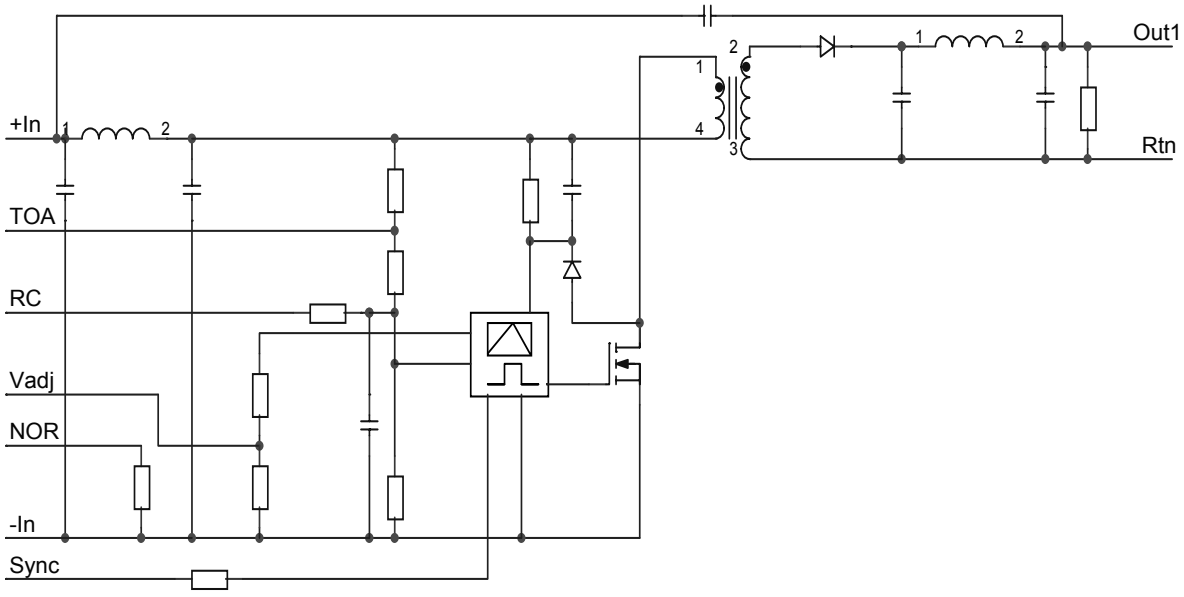
Absolute Maximum Ratings

Characteristics			min	typ	max	Unit
T_{ref}	Operating Temperature (see Thermal Consideration section)		-45		+110	°C
T_S	Storage temperature		-55		+125	°C
V_I	Input voltage		-0.5		+75	V
V_{iso}	Isolation voltage (input to output test voltage)				1500	Vdc
V_{tr}	Input voltage transient (Tp 100 ms)				100	V
V_{RC}	Remote Control pin voltage (see Operating Information section)	Positive logic option	-5		+16	V
V_{adj}	Adjust pin voltage (see Operating Information section)		-5		+40	V

Stress in excess of Absolute Maximum Ratings may cause permanent damage. Absolute Maximum Ratings, sometimes referred to as no destruction limits, are normally tested with one parameter at a time exceeding the limits of Output data or Electrical Characteristics. If exposed to stress above these limits, function and performance may degrade in an unspecified manner.

Fundamental Circuit Diagram



3.3 V/3 A Electrical Specification — PKR 4910A SI

T_{ref} = -30 to +95ºC, V_I = 36 to 75 V, unless otherwise specified under Conditions.
Typical values given at: T_{ref} = +25°C, V_I= 53 V, max I_O , unless otherwise specified under Conditions.

Characteristics		Conditions	min	typ	max	Unit
V_I	Input voltage range		36		75	V
V_{Ioff}	Turn-off input voltage	Decreasing input voltage	30	33.5	35	V
V_{Ion}	Turn-on input voltage	Increasing input voltage	32	34.5	36	V
C_I	Internal input capacitance			2		µF
P_O	Output power	Output voltage initial setting	0		9.9	W
SVR	Supply voltage rejection (ac)	f = 100 Hz sinewave, 1 Vp-p		79		dB
η	Efficiency	50 % of max I_O		79		%
		max I_O		80		
		50 % of max I_O , V_I = 48 V		80		
		max I_O , V_I = 48 V		80		
P_d	Power Dissipation	max I_O		2.4	2.7	W
P_{li}	Input idling power	I_O= 0, V_I = 53 V		217		mW
P_{RC}	Input standby power	V_I = 53 V (turned off with RC)		55		mW
f_s	Switching frequency	10 -100% of max I_O	477	510	533	kHz

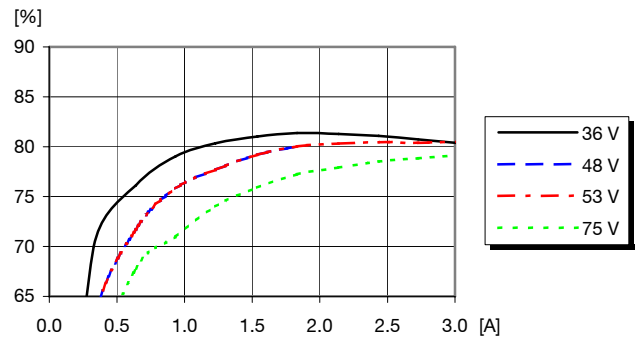
V_{Oi}	Output voltage initial setting and accuracy	T_{ref} = +25°C, V_I = 53 V, I_O = 2.0 A	3.27	3.30	3.33	V
	Output adjust range		1.75		4.08	V
V_O	Output voltage tolerance band	10-100% of max I_O	3.13		3.43	V
	Idling voltage	I_O = 0	3.50		4.0	V
	Line regulation	max I_O		13	20	mV
	Load regulation	V_I = 53 V, 10-100% of max I_O		86	170	mV
V_{tr}	Load transient voltage deviation	V_I = 53 V, Load step 25-75-25 % of max I_O, di/dt = 1 A/µs, see Note 1		±260		mV
t_{tr}	Load transient recovery time			30		µs
t_r	Ramp-up time (from 10–90 % of V_{oi})	10-100% of max I_O	0.2	1.8	5	ms
t_s	Start-up time (from V_I connection to 90% of V_{oi})		1	5	15	ms
I_O	Output current		0		3	A
I_{lim}	Current limit threshold	Vo = 3.0 V, T_{ref} < max T_{ref}	3.2	3.5	4	A
I_{sc}	Short circuit current	T_{ref} = 25ºC		4.1	5	A
V_{Oac}	Output ripple & noise	See ripple & noise section, max I_O, V_O.		6	50	mVp-p

Note 1: Output filter according to Ripple & Noise section

3.3 V/3 A Typical Characteristics

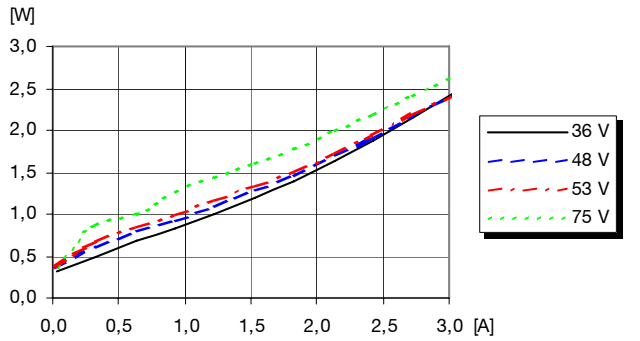
PKR 4910A SI

Efficiency



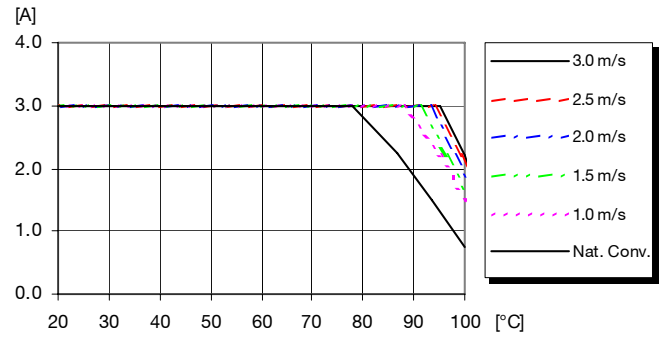
Efficiency vs. load current and input voltage at T_{ref} = +25°C

Power Dissipation



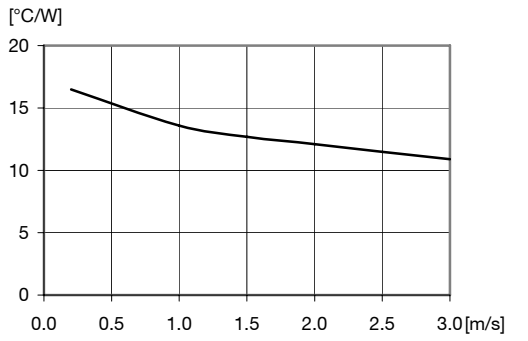
Dissipated power vs. load current and input voltage at T_{ref} = +25°C

Output Current Derating



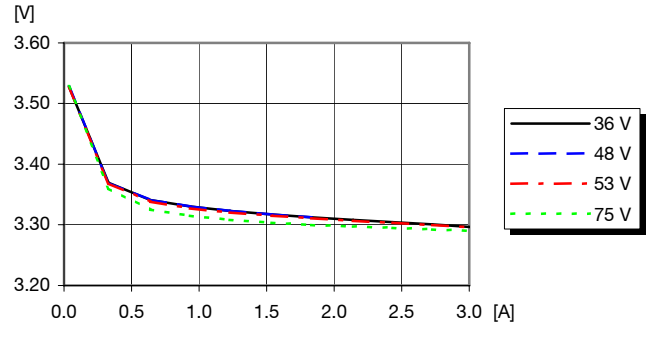
Available load current vs. ambient air temperature and airflow at V_I = 53 V. See Thermal Consideration section.

Thermal Resistance



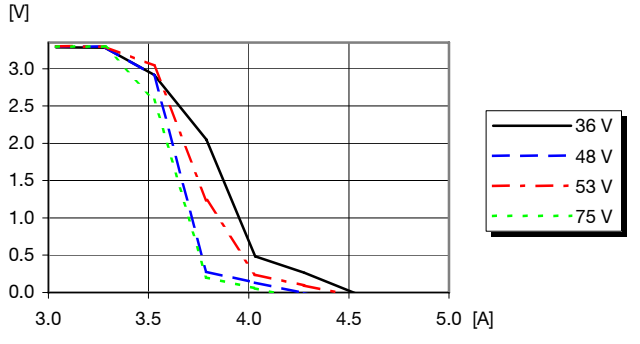
Thermal resistance vs. airspeed measured at the converter. Tested in wind tunnel with airflow and test conditions as per the Thermal consideration section.

Output Characteristics



Output voltage vs. load current at T_{ref} = +25°C

Current Limit Characteristics

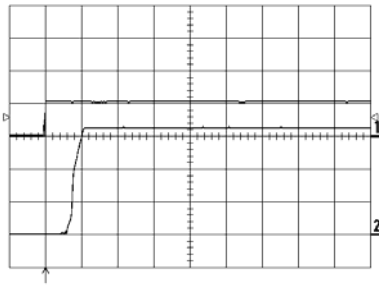


Output voltage vs. load current at I_O > max I_O, T_{ref} = +25°C

3.3 V/3 A Typical Characteristics

PKR 4910A SI

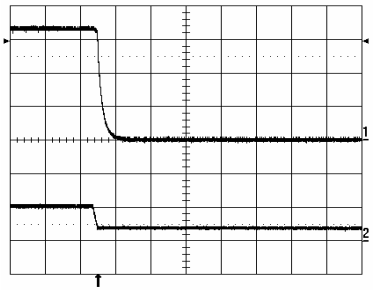
Start-up



Start-up enabled by connecting V_I at: T_{ref} = +25°C, I_O = 3 A resistive load, V_I = 53 V.

Top trace: output voltage (1 V/div.). Bottom trace: input voltage (50 V/div.). Time scale: 5 ms/div..

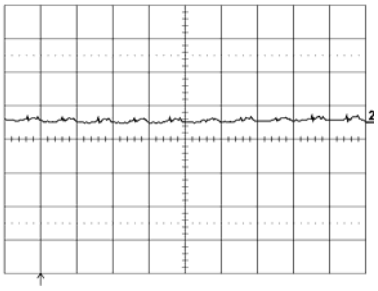
Shut-down



Shut-down enabled by disconnecting V_I at: T_{ref} = +25°C, I_O = 3 A resistive load, V_I = 53 V

Top trace: output voltage (1 V/div.). Bottom trace: input voltage (50 V/div.). Time scale: 1 ms/div..

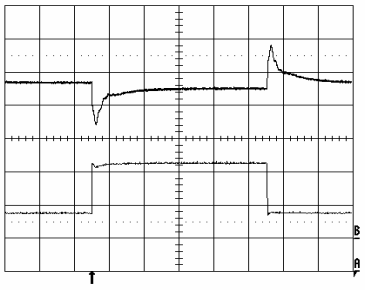
Output Ripple & Noise



Output voltage ripple (20mV/div.) at: T_{ref} = +25°C, I_O = 3 A resistive load, V_I = 53 V. Time scale: 2 µs/div.

See the filter in the Output ripple and noise section (EMC Specification).

Output Load Transient Response



Output voltage response to load current step-change (0.75-2.25-0.75 A) at: T_{ref} =+25°C, V_I = 53 V.

Top trace: output voltage (200mV/div.). Bottom trace: load current (1 A/div.). Time scale: 0.1 ms/div.

Output Voltage Adjust (see operating information)

Passive trim

The resistor value for an adjusted output voltage is calculated by using the following equations:

Output Voltage Adjust Upwards, Increase:

R_{ou}= 4.20 x (4.13 – V_o) / (V_o - V_{oi}) kΩ

Eg Increase 4% =>V_{out} =3.43 Vdc
4.20 x (4.13 – 3.43) / (3.43 – 3.30) = 22.6 kΩ

Output Voltage Adjust Downwards, Decrease:

R_{od}= 17.6 x (V_{oi} – V_o) / (V_o –1,75) kΩ

Eg Decrease 2% =>V_{out} = 3.23 Vdc
17.6 x (3.3 – 3.23) / (3.23 –1.75)= *0.832 kΩ*

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Technical Specification

PKR 4000A SI series DC/DC converters, Input 36-75 V, Output 3 A/15 W	EN/LZT 146 300 R5B April 2006

5 V/3 A Electrical Specification — PKR 4211A SI

T_{ref} =-30 to +90ºC, V_I = 36 to 75 V, unless otherwise specified under Conditions.
Typical values given at: T_{ref} = +25°C, V_I= 53 V, max I_O , unless otherwise specified under Conditions.

Characteristics		Conditions	min	typ	max	Unit
V_I	Input voltage range		36		75	V
V_{Ioff}	Turn-off input voltage	Decreasing input voltage	30	33.5	35	V
V_{Ion}	Turn-on input voltage	Increasing input voltage	32	34.5	36	V
C_I	Internal input capacitance			2		µF
P_O	Output power	Output voltage initial setting	0		15	W
SVR	Supply voltage rejection (ac)	f = 100 Hz sinewave, 1 Vp-p		71		dB
η	Efficiency	50 % of max I_O		83		%
		max I_O		84		
		50 % of max I_O , V_I = 48 V		83.5		
		max I_O , V_I = 48 V		84		
P_d	Power Dissipation	max I_O		2.8	3.3	W
P_{Ii}	Input idling power	I_O= 0, V_I = 53 V		0.25		W
P_{RC}	Input standby power	V_I = 53 V (turned off with RC)		0.055		W
f_s	Switching frequency	0-100% of max I_O	477	510	533	kHz

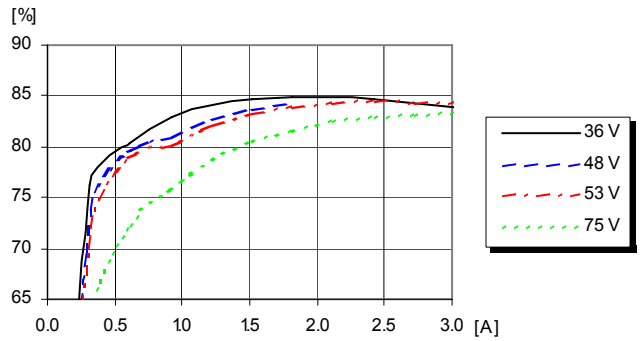
V_{Oi}	Output voltage initial setting and accuracy	T_{ref} = +25°C, V_I = 53 V, I_O= 1.5A	5.01	5.05	5.09	V
	Output adjust range		2.8		6.3	V
V_O	Output voltage tolerance band	10-100% of max I_O	4.85		5.25	V
	Idling voltage	I_O = 0	5.05		6.1	V
	Line regulation	max I_O		15	32	mV
	Load regulation	V_I = 53 V, 10-100% of max I_O		51	140	mV
V_{tr}	Load transient voltage deviation	V_I = 53 V, Load step 25-75-25 % of max I_O, di/dt = 1 A/µs, see Note 1		±330		mV
t_{tr}	Load transient recovery time			50		µs
t_r	Ramp-up time (from 10–90 % of V_{oi})	10-100% of max I_O	0.2	1.8	5	ms
t_s	Start-up time (from V_I connection to 90% of V_{oi})		1	5	15	ms
I_O	Output current		0		3	A
I_{lim}	Current limit threshold	Vo = 4.0 V, T_{ref} < max T_{ref}	3.3	3.8	4.1	A
I_{sc}	Short circuit current	T_{ref} = 25ºC		4.8	6	A
V_{Oac}	Output ripple & noise	See ripple & noise section, max I_O, V_{Oi}		8	50	mVp-p

Note 1: Output filter according to Ripple & Noise section

5 V/3 A Typical Characteristics

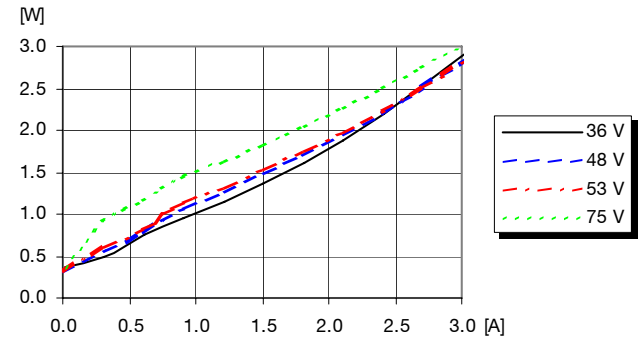
PKR 4211A SI

Efficiency



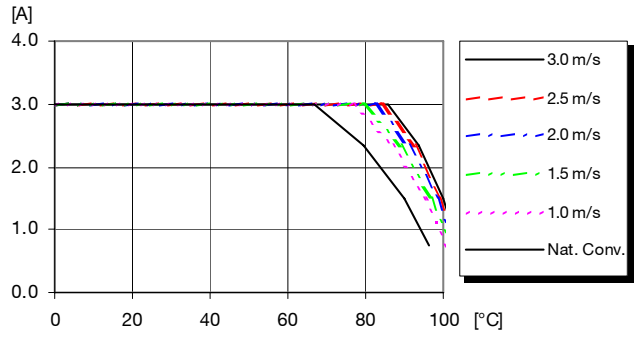
Efficiency vs. load current and input voltage at T_{ref} = +25°C

Power Dissipation



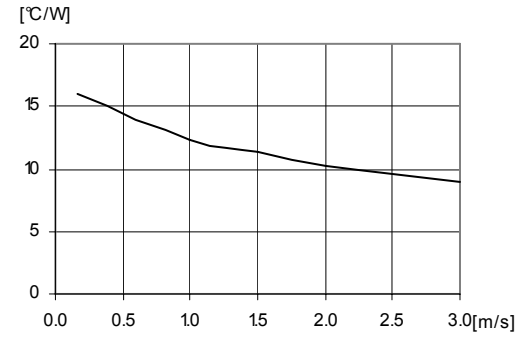
Dissipated power vs. load current and input voltage at T_{ref} = +25°C

Output Current Derating



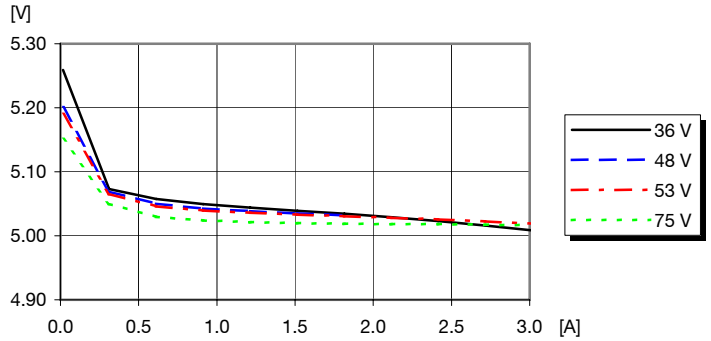
Available load current vs. ambient air temperature and airflow at V_I = 53 V. See Thermal Consideration section.

Thermal Resistance



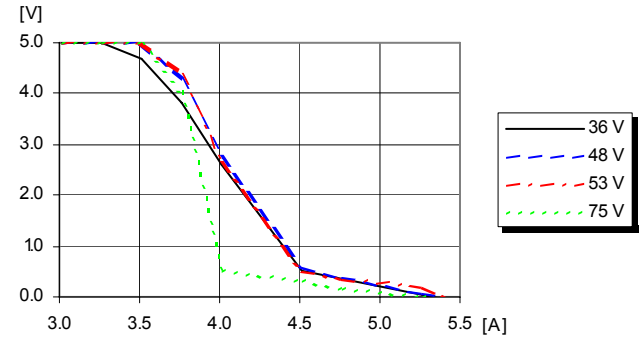
Thermal resistance vs. airspeed measured at the converter. Tested in wind tunnel with airflow and test conditions as per the Thermal consideration section.

Output Characteristics



Output voltage vs. load current at T_{ref} = +25°C

Current Limit Characteristics

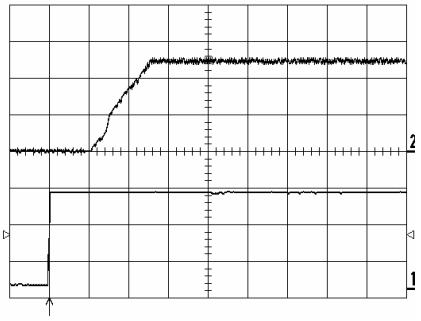


Output voltage vs. load current at I_O > max I_O, T_{ref} = +25°C

5 V/3 A Typical Characteristics

PKR 4211A SI

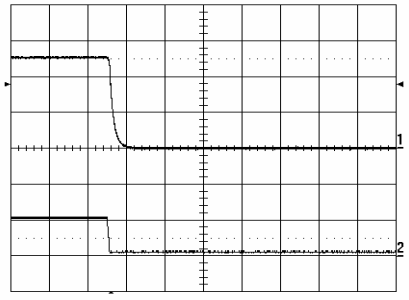
Start-up



Start-up enabled by connecting V_I at: T_{ref} = +25°C, I_O = 3 A resistive load, V_I = 53 V.

Top trace: output voltage (2.0 V/div.). Bottom trace: input voltage (20 V/div.). Time scale: 2 ms/div.

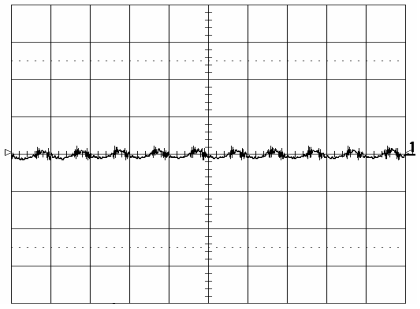
Shut-down



Shut-down enabled by disconnecting V_I at: T_{ref} = +25°C, I_O = 3 A resistive load, V_I = 53 V.

Top trace: output voltage (2 V/div.). Bottom trace: input voltage (50 V/div.). Time scale: 2 ms/div.

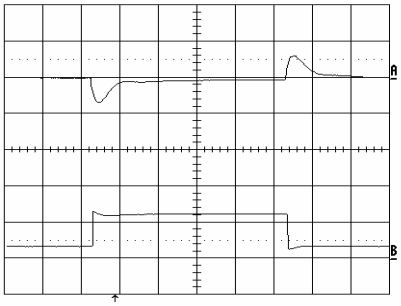
Output Ripple & Noise



Output voltage ripple (20mV/div.) at: T_{ref} = +25°C, I_O = 3 A resistive load, V_I = 53 V. Time scale: 2 µs/div.

See the filter in the Output ripple and noise section (EMC Specification).

Output Load Transient Response



Output voltage response to load current step-change (.5-22.5-7.5 A) at: T_{ref} =+25°C, V_I = 53 V.

Top trace: output voltage (500mV/div.). Bottom trace: load current (2.0 A/div.). Time scale: 0.1 ms/div.

Output Voltage Adjust (see operating information)

Passive trim

The resistor value for an adjusted output voltage is calculated by using the following equations:

Output Voltage Adjust Upwards, Increase:

R_{ou}= 4.20 x (6.35 – V_o) / (V_o - V_{oi}) kΩ

Eg Increase 4% =>V_{out} =5.25 Vdc
4.20 x (6.35 – 5.25) / (5.25 – 5.05) = 23.1 kΩ

Output Voltage Adjust Downwards, Decrease:

R_{od}= 18 x (V_{oi} – V_o) / (V_o –2.7) kΩ

Eg Decrease 2% =>V_{out} = 4.95 Vdc
18 x (5.05 – 4.95) / (4.95 –2.7)= *0.8 kΩ*

7 V/2.2 A Electrical Specification — PKR 4117A SI

T_{ref} = -30 to +95ºC, V_I = 36 to 75 V, unless otherwise specified under Conditions.
Typical values given at: T_{ref} = +25°C, V_I= 53 V_I max I_O, unless otherwise specified under Conditions.

Characteristics		Conditions	min	typ	max	Unit
V_I	Input voltage range		36		75	V
V_{Ioff}	Turn-off input voltage	Decreasing input voltage	30	33.5	35	V
V_{Ion}	Turn-on input voltage	Increasing input voltage	32	34.5	36	V
C_I	Internal input capacitance			2		µF
P_O	Output power	Output voltage initial setting	0		15.4	W
SVR	Supply voltage rejection (ac)	f = 100 Hz sinewave, 1 Vp-p		68		dB
η	Efficiency	50 % of max I_O		82.5		%
		max I_O		83.0		
		50 % of max I_O, V_I = 48 V		83.0		
		max I_O, V_I = 48 V		83.0		
P_d	Power Dissipation	max I_O		3.2	3.9	W
P_{Ii}	Input idling power	I_O = 0 A, V_I = 53 V		256		mW
P_{RC}	Input standby power	V_I = 53 V (turned off with RC)		55		mW
f_s	Switching frequency	0-100 % of max I_O	477	510	533	kHz

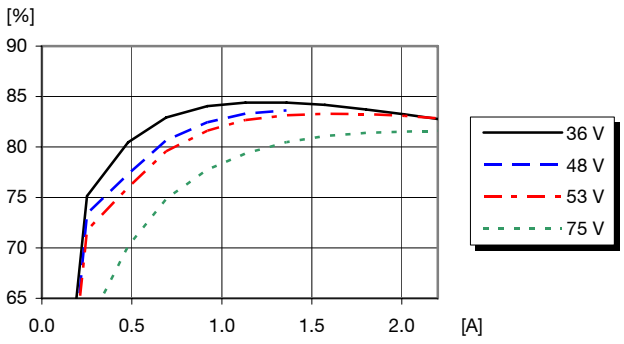
V_{Oi}	Output voltage initial setting and accuracy	T_{ref} = +25°C, V_I = 53 V, I_O = 1.5 A	7.0	7.08	7.16	V
V_O	Output adjust range	See operating information	4.0		8.75	V
	Output voltage tolerance band	10-100 % of max I_O	6.72		7.4	V
	Idling voltage	I_O = 0 A	7.2		8.1	V
	Line regulation	max I_O		7	50	mV
	Load regulation	V_I = 53 V, 10-100 % of max I_O		150	190	mV
V_{tr}	Load transient voltage deviation	V_I = 53 V, Load step 25-75-25 % of max I_O, di/dt = 1 A/µs		±250		mV
t_{tr}	Load transient recovery time			150		µs
t_r	Ramp-up time (from 10–90 % of V_{Oi})	10-100 % of max I_O	0.2	1.8	5	ms
t_s	Start-up time (from V_I connection to 90 % of V_{Oi})		1	5	15	ms
I_O	Output current		0		2.2	A
I_{lim}	Current limit threshold	V_o = 6 V, T_{ref} < max T_{ref}	2.5	2.8	3.3	A
I_{sc}	Short circuit current	T_{ref} = 25ºC,		3.3	4	A
V_{Oac}	Output ripple & noise	See ripple & noise section, max I_O, V_{Oi}		8	50	mVp-p

Note 1: Output filter according to ripple & noise section

7 V/2.2 A Typical Characteristics

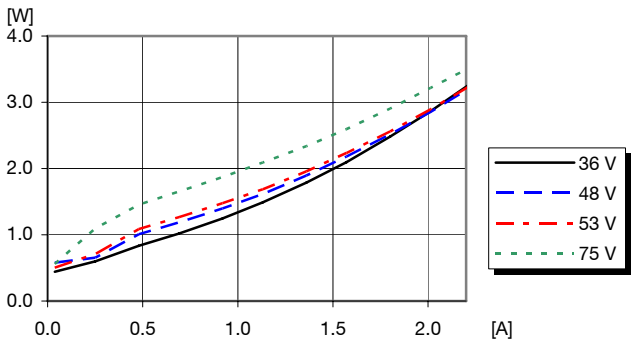
PKR 4117A SI

Efficiency



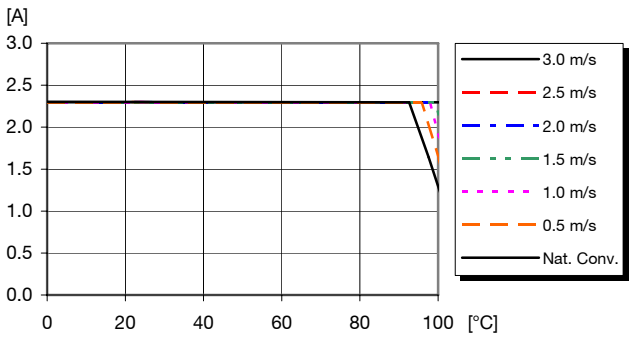
Efficiency vs. load current and input voltage at T_{ref} = +25°C

Power Dissipation



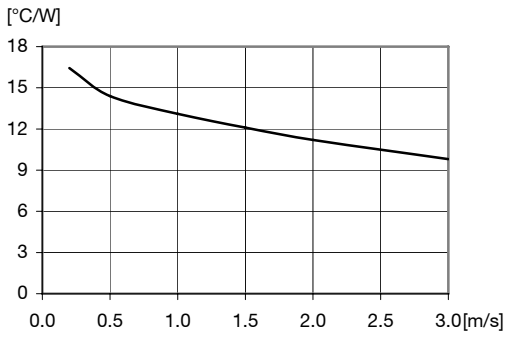
Dissipated power vs. load current and input voltage at T_{ref} = +25°C

Output Current Derating



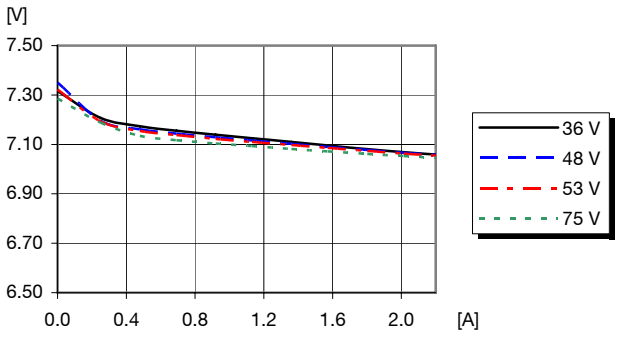
Available load current vs. ambient air temperature and airflow at V_I = 53 V. See Thermal Consideration section.

Thermal Resistance



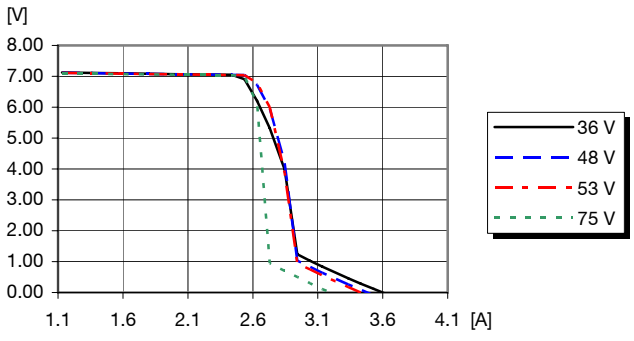
Thermal resistance vs. airspeed measured at the converter. Tested in wind tunnel with airflow and test conditions as per the Thermal consideration section.

Output Characteristics



Output voltage vs. load current at T_{ref} = +25°C

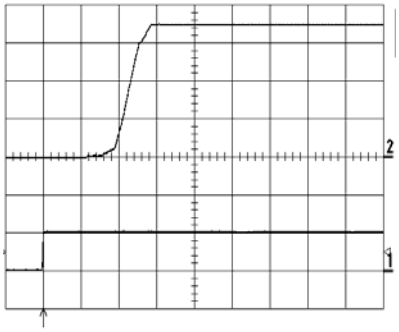
Current Limit Characteristics



Output voltage vs. load current at I_O > max I_O , T_{ref} = +25°C

7 V/2.2 A Typical Characteristics — PKR 4117A SI

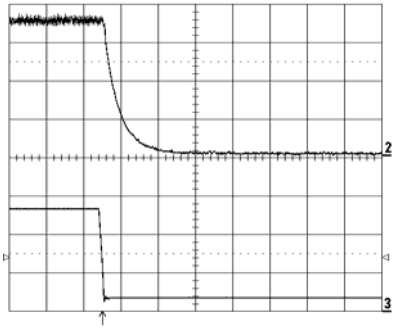
Start-up



Start-up enabled by connecting V_I at:
T_{ref} = +25°C, V_I = 53 V,
I_O = 2.2 A resistive load.

Top trace: output voltage (2.0 V/div.).
Bottom trace: input voltage (50 V/div.).
Time scale: (2 ms/div.).

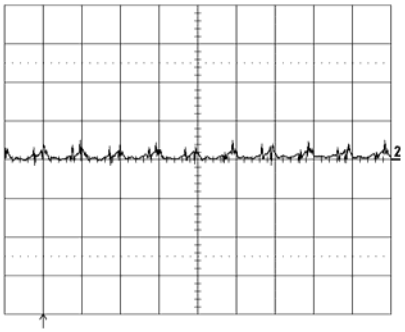
Shut-down



Shut-down enabled by disconnecting V_I at:
T_{ref} = +25°C, V_I = 53 V,
I_O = 2.2 A resistive load.

Top trace: output voltage (2.0 V/div.).
Bottom trace: input voltage (20 V/div.).
Time scale: (1.0 ms/div.).

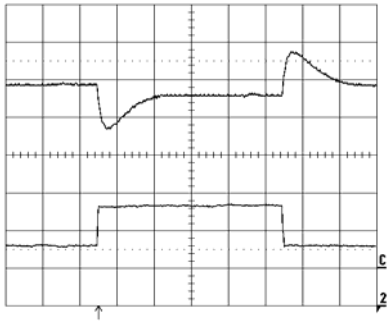
Output Ripple & Noise



Output voltage ripple at:
T_{ref} = +25°C, V_I = 53 V,
I_O = 2.2 A resistive load.

Trace: output voltage (10 mV/div.).
Time scale: (2 µs/div.).

Output Load Transient Response



Output voltage response to load current step-change (0.55-1.65-0.55 A) at:
T_{ref} =+25°C, V_I = 53 V.

Top trace: output voltage (200 mV/div.).
Bottom trace: load current (1.5 A/div.).
Time scale: (0.1 ms/div.).

Output Voltage Adjust (see operating information)

Passive trim
The resistor value for an adjusted output voltage is calculated by using the following equations:

Output Voltage Adjust Upwards, Increase:
R_{ou}= 4.20 x (8.9 – V_o) / (V_o - V_{oi}) kΩ

Eg Increase 4% => V_{out} = 7.28 Vdc
4.20 x (8.9 – 7.28) / (7.28 – 7.08) = 34 kΩ

Output Voltage Adjust Downwards, Decrease:
R_{od}= 18 x (V_{oi} – V_o) / (V_o –3.93) kΩ

Eg Decrease 2% =>V_{out} = 6.86 Vdc
18 x (7.08 – 6.86) / (6.86 –3.93) = *1.35 kΩ*

ERICSSON

Technical Specification

PKR 4000B SI series DC/DC converters, Input 36-75 V, Output 5 A/14.5 W	EN/LZT 146 301 R5A March 2006

Key Features

- Industry standard MacroDens™ footprint 47.8 x 28.1 x max height 8.0 mm (1.88 x 1.11 x max height 0.32 in.)
- High efficiency, typ. 86 % at 3.3 Vout full load
- 1500 Vdc input to output isolation
- Meets isolation requirements equivalent to basic insulation according to IEC/EN/UL 60950
- More than 4.5 million hours predicted MTBF at 40°C ambient temperature

General Characteristics

- Suited for narrow board pitch applications (15 mm/0.6 in)
- Over temperature protection
- Over current protection
- Output short-circuit protection
- Soft start
- Remote control
- Output voltage adjust function
- Highly automated manufacturing to ensure highest quality
- ISO 9001/14001 certified supplier

Safety Approvals

Design for Environment

Meets requirements in high-temperature lead-free soldering processes.

Contents

General Information 2
Safety Specification 3
Absolute Maximum Ratings 4

Product Program	Ordering No.	
1.8 V/5.0 A Electrical Specification	PKR 4918B SI	5
2.5 V/4.4 A Electrical Specification	PKR 4919B SI	8
3.3 V/4.5 A Electrical Specification	PKR 4110B SI	11

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Thermal Consideration 17
Connections 18
Mechanical Information 19
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Delivery Information 22
Product Qualification Specification 23

General Information

Ordering Information

See Contents for individual product ordering numbers.

Reliability

The Mean Time Between Failure (MTBF) is calculated at full output power and an operating ambient temperature (T_A) of +40°C, which is a typical condition in Information and Communication Technology (ICT) equipment. Different methods could be used to calculate the predicted MTBF and failure rate which may give different results. Ericsson Power Modules currently uses one method, Telcordia SR332.

Predicted MTBF for the series is:

- 4.5 million hours according to Telcordia SR332, issue 1, Black box technique.

Telcordia SR332 is a commonly used standard method intended for reliability calculations in ICT equipment. The parts count procedure used in this method was originally modelled on the methods from MIL-HDBK-217F, Reliability Predictions of Electronic Equipment. It assumes that no reliability data is available on the actual units and devices for which the predictions are to be made, i.e. all predictions are based on generic reliability parameters.

Compatibility with RoHS requirements

The products are compatible with the relevant clauses and requirements of the RoHS directive 2002/95/EC and have a maximum concentration value of 0.1% by weight in homogeneous materials for lead in other applications other than lead in solder, lead in high melting temperature type solder, lead in glass of electronics components, lead in electronic ceramic parts and lead as an alloying element in copper containing up to 4% lead by weight, mercury, hexavalent chromium, PBB and PBDE and of 0.01% by weight in homogeneous materials for cadmium.

Exemptions in the RoHS directive utilized in the products:

- Lead as an alloying element in copper alloy containing up to 4% lead by weight (used in connection pins made of Brass)
- Lead in high melting temperature type solder (used to solder the die in semiconductor packages)
- Lead in glass of electronics components and in electronic ceramic parts (e.g. fill material in chip resistors)
- Lead in solder for servers, storage and storage array systems, network infrastructure equipment for switching, signaling, transmission as well as network management for telecommunication
  (Note: the products are manufactured in lead-free soldering processes and the lead present in the solder is only located in the terminal plating finishes on some components)

Quality Statement

The products are designed and manufactured in an industrial environment where quality systems and methods like ISO 9000, 6σ (sigma), and SPC are intensively in use to boost the continuous improvements strategy. Infant mortality or early failures in the products are screened out and they are subjected to an ATE-based final test. Conservative design rules, design reviews and product qualifications, plus the high competence of an engaged work force, contribute to the high quality of our products.

Warranty

Warranty period and conditions are defined in Ericsson Power Modules General Terms and Conditions of Sale.

Limitation of Liability

Ericsson Power Modules does not make any other warranties, expressed or implied including any warranty of merchantability or fitness for a particular purpose (including, but not limited to, use in life support applications, where malfunctions of product can cause injury to a person's health or life).

Safety Specification

General information
Ericsson Power Modules DC/DC converters and DC/DC regulators are designed in accordance with safety standards IEC/EN/UL60950, *Safety of Information Technology Equipment*.

IEC/EN/UL60950 contains requirements to prevent injury or damage due to the following hazards:

- Electrical shock
- Energy hazards
- Fire
- Mechanical and heat hazards
- Radiation hazards
- Chemical hazards

On-board DC-DC converters are defined as component power supplies. As components they cannot fully comply with the provisions of any Safety requirements without "Conditions of Acceptability". It is the responsibility of the installer to ensure that the final product housing these components complies with the requirements of all applicable Safety standards and Directives for the final product.

Component power supplies for general use should comply with the requirements in IEC60950, EN60950 and UL60950 *"Safety of information technology equipment".*

There are other more product related standards, e.g. IEEE802.3af "Ethernet LAN/MAN Data terminal equipment power", and ETS300132-2 "Power supply interface at the input to telecommunications equipment; part 2: DC", but all of these standards are based on IEC/EN/UL60950 with regards to safety.

Ericsson Power Modules DC/DC converters and DC/DC regulators are UL60950 recognized and certified in accordance with EN60950.

The flammability rating for all construction parts of the products meets requirements for V-0 class material according to IEC 60695-11-10.

The products should be installed in the end-use equipment, in accordance with the requirements of the ultimate application. Normally the output of the DC/DC converter is considered as SELV (Safety Extra Low Voltage) and the input source must be isolated by minimum Double or Reinforced Insulation from the primary circuit (AC mains) in accordance with IEC/EN/UL60950.

Isolated DC/DC converters
It is recommended that a slow blow fuse with a rating twice the maximum input current per selected product be used at the input of each DC/DC converter. If an input filter is used in the circuit the fuse should be placed in front of the input filter.
In the rare event of a component problem in the input filter or in the DC/DC converter that imposes a short circuit on the input source, this fuse will provide the following functions:

- Isolate the faulty DC/DC converter from the input power source so as not to affect the operation of other parts of the system.
- Protect the distribution wiring from excessive current and power loss thus preventing hazardous overheating.

The galvanic isolation is verified in an electric strength test. The test voltage (V_{iso}) between input and output is 1500 Vdc or 2250 Vdc for 60 seconds (refer to product specification).
Leakage current is less than 1 µA at nominal input voltage.

24 V DC systems
The input voltage to the DC/DC converter is SELV (Safety Extra Low Voltage) and the output remains SELV under normal and abnormal operating conditions.

48 and 60 V DC systems
If the input voltage to Ericsson Power Modules DC/DC converter is 75 Vdc or less, then the output remains SELV (Safety Extra Low Voltage) under normal and abnormal operating conditions.

Single fault testing in the input power supply circuit should be performed with the DC/DC converter connected to demonstrate that the input voltage does not exceed 75 Vdc.

If the input power source circuit is a DC power system, the source may be treated as a TNV2 circuit and testing has demonstrated compliance with SELV limits and isolation requirements equivalent to Basic Insulation in accordance with IEC/EN/UL60950.

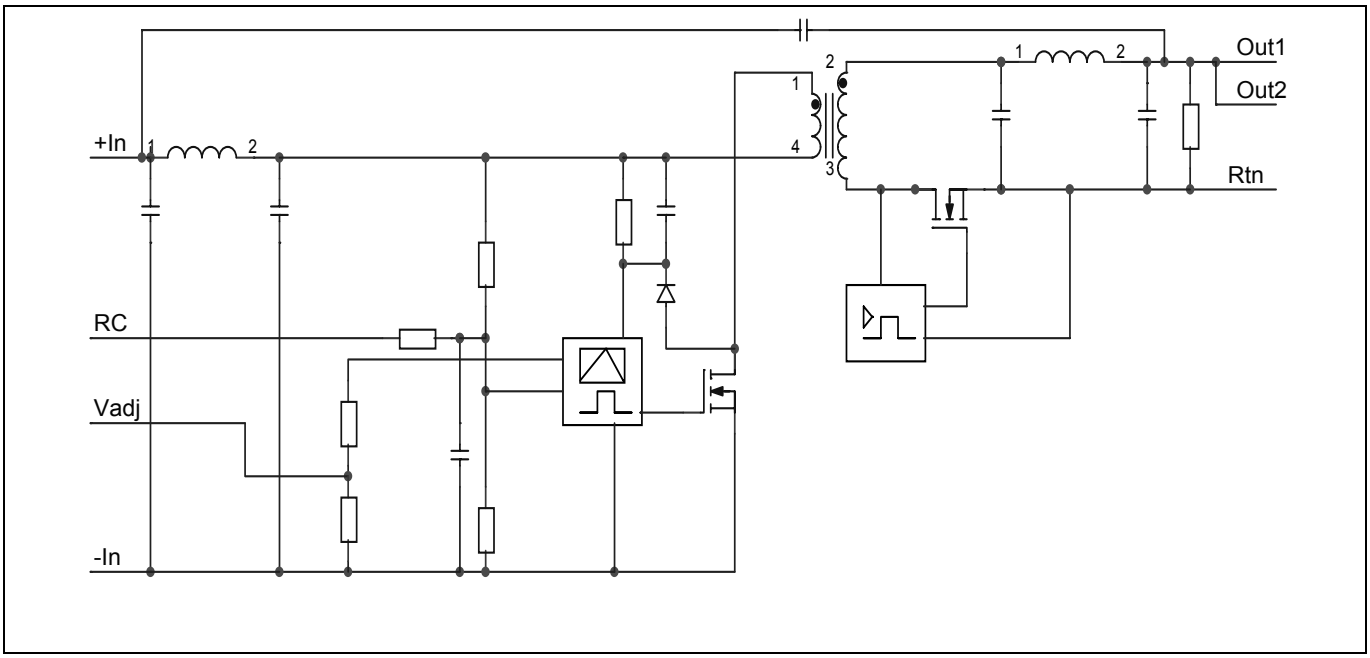
Non-isolated DC/DC regulators
The input voltage to the DC/DC regulator is SELV (Safety Extra Low Voltage) and the output remains SELV under normal and abnormal operating conditions.

Absolute Maximum Ratings

Characteristics			min	typ	max	Unit
T_{ref}	Operating Temperature (see Thermal Consideration section)		-45		+110	°C
T_S	Storage temperature		-55		+125	°C
V_I	Input voltage		-0.5		+75	V
V_{iso}	Isolation voltage (input to output test voltage)				1500	Vdc
V_{tr}	Input voltage transient (Tp 100 ms)				100	V
V_{RC}	Remote Control pin voltage (see Operating Information section)	Positive logic option	-5		+16	V
V_{adj}	Adjust pin voltage (see Operating Information section)		-5		+40	V

Stress in excess of Absolute Maximum Ratings may cause permanent damage. Absolute Maximum Ratings, sometimes referred to as no destruction limits, are normally tested with one parameter at a time exceeding the limits of Output data or Electrical Characteristics. If exposed to stress above these limits, function and performance may degrade in an unspecified manner.

Fundamental Circuit Diagram



1.8 V / 5 A Electrical Specification — PKR4918B SI

T_{ref} = -30 to +95ºC, V_I = 36 to 75 V, pin 8 connected to pin 9 unless otherwise specified under Conditions.
Typical values given at: T_{ref} = +25°C, V_I= 53 V, max I_O, unless otherwise specified under Conditions.

Characteristics		Conditions	min	typ	Max	Unit
V_I	Input voltage range		36		75	V
V_{Ioff}	Turn-off input voltage	Decreasing input voltage	30	33.5	35	V
V_{Ion}	Turn-on input voltage	Increasing input voltage	32	34.5	36	V
C_I	Internal input capacitance			2		µF
P_O	Output power	Output voltage initial setting	0		9	W
SVR	Supply voltage rejection (ac)	f = 100 Hz sine wave, 1 Vp-p		65		dB
η	Efficiency	50 % of max I_O		82		%
		max I_O		81		
		50 % of max I_O , V_I = 48 V		83		
		max I_O , V_I = 48 V		81		
P_d	Power Dissipation	max I_O		2.2	2.9	W
P_{li}	Input idling power	I_O = 0 A , V_I = 53 V		0.47		W
P_{RC}	Input standby power	V_I = 53V (turned off with RC)		43		mW
f_s	Switching frequency	0-100% of max I_O	290	305	325	kHz

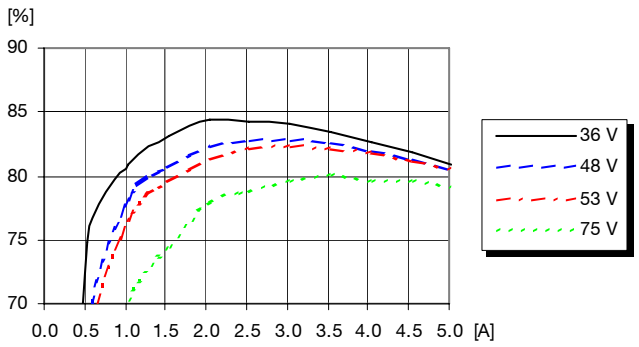
V_{Oi}	Output voltage initial setting and accuracy	T_{ref} = +25°C, V_I = 53 V, max I_O	1.77	1.8	1.84	V
	Output adjust range		1.5		2.2	V
V_O	Output voltage tolerance band	10-100% of max I_O	1.71		1.89	V
	Idling voltage	I_O = 0 A	1.69		1.91	V
	Line regulation	max I_O		15	60	mV
	Load regulation	V_I = 53 V, 10-100% of max I_O		20	100	mV
V_{tr}	Load transient voltage deviation	V_I = 53 V, Load step 25-75-25 % of max I_O, di/dt = 1 A/µs, see Note 1		±250		mV
t_{tr}	Load transient recovery time			0.1		ms
t_r	Ramp-up time (from 10–90 % of V_{Oi})	10-100% of max I_O	1	1.8	3	ms
t_s	Start-up time (from V_I connection to 90% of V_{Oi})		3	5	11	ms
I_O	Output current		0		5	A
I_{lim}	Current limit threshold	Vo = 1.5V, T_{ref} < max T_{ref}	5.2	6.1	6.4	A
I_{sc}	Short circuit current	T_{ref} = 25ºC		6.2	7.0	A
V_{Oac}	Output ripple & noise	See ripple & noise section, max I_O, V_{Oi}		10	30	mVp-p

Note 1: Output filter according to Ripple & Noise section

1.8 V / 5 A Typical Characteristics

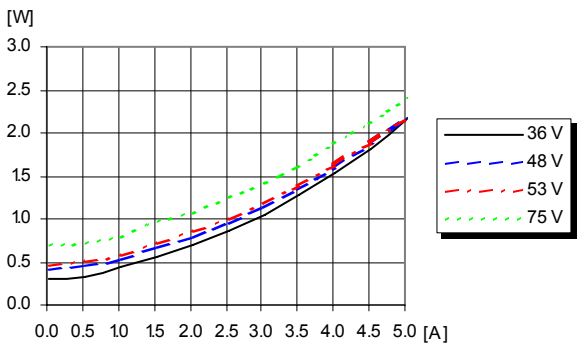
PKR4918B SI

Efficiency



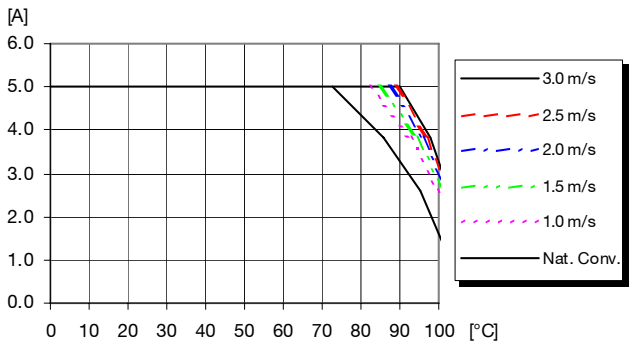
Efficiency vs. load current and input voltage at T_{ref} = +25°C

Power Dissipation



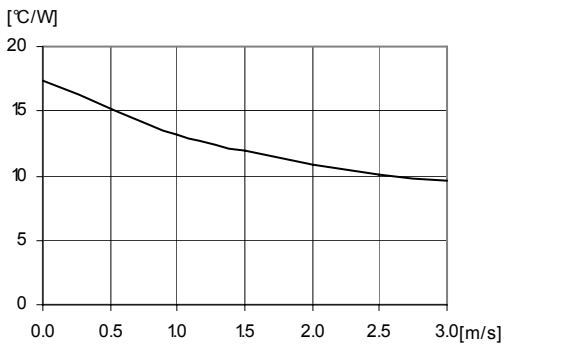
Dissipated power vs. load current and input voltage at T_{ref} = +25°C

Output Current Derating



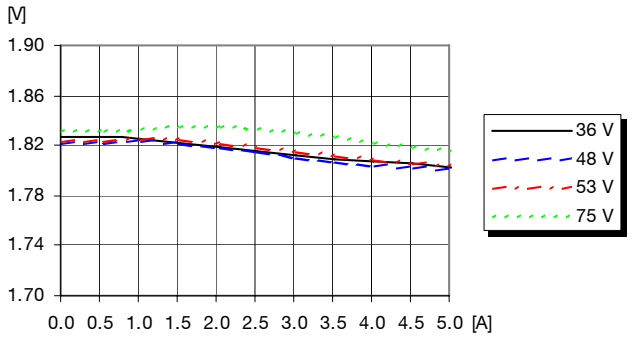
Available load current vs. ambient air temperature and airflow at V_I = 53 V. See Thermal Consideration section.

Thermal Resistance



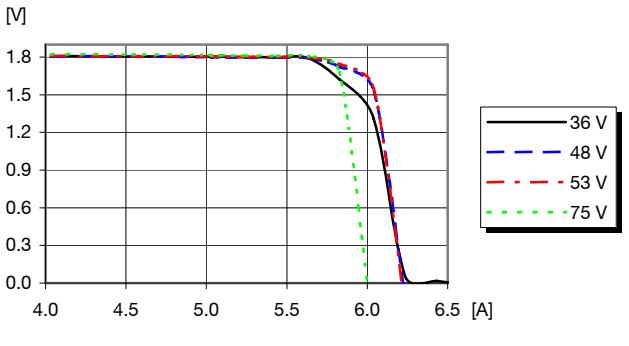
Thermal resistance vs. airspeed measured at the converter. Tested in wind tunnel with airflow and test conditions as per the Thermal consideration section.

Output Characteristics



Output voltage vs. load current at T_{ref} = +25°C

Current Limit Characteristics

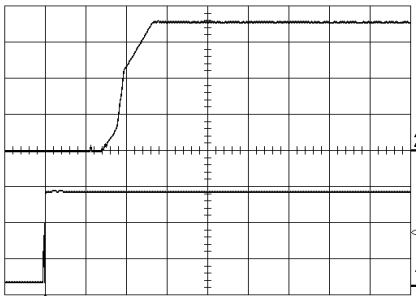


Output voltage vs. load current at I_O > max I_O , T_{ref} = +25°C

1.8 V / 5A Typical Characteristics

PKR 4918B SI

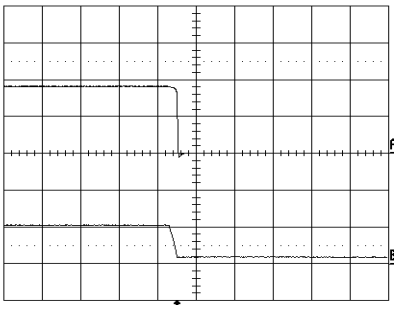
Start-up



Start-up enabled by connecting V_I at: T_{ref} = +25°C, I_O = 5 A resistive load, V_I = 53 V.

Top trace: output voltage (0.5 V/div.). Bottom trace: input voltage (20 V/div.). Time scale: 2 ms/div.

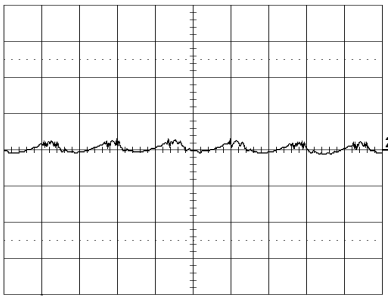
Shut-down



hut-down enabled by disconnecting V_I at: T_{ref} = +25°C, I_O = 5 A resistive load, V_I = 53 V.

Top trace: output voltage (1 V/div.). Bottom trace: input voltage (50 V/div.). Time scale: 1 ms/div.

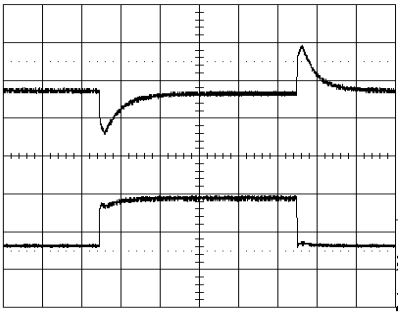
Output Ripple & Noise



Output voltage ripple ({20mV/div.}) at: T_{ref} = +25°C, I_O = 5 A resistive load, V_I = 53 V. Time scale: 2 µs/div.

See the filter in the Output ripple and noise section (EMC Specification).

Output Load Transient Response



Output voltage response to load current step-change (1.25-3.75-1.25 A}) at: T_{ref}=+25°C, V_I = 53 V.

Top trace: output voltage (200mV/div). Bottom trace: load current (2 A/div). Time scale: 0.1 ms/div..

Output Voltage Adjust (see operating information)

Passive trim

The resistor value for an adjusted output voltage is calculated by using the following equations:

To adjust the output voltage upwards, a resistor is connected between pins 8 and 17. Pins 8 and 9 have to be shorted. The output voltage increases when the resistance decreases. The resistance value is given by the equation:

Rou= 4.14 × (1.28V_{oi}– V_{od})/(V_{od} – V_{oi}),(kOhm); Vod is the desired output voltage and Voi is the initial output voltage.

Active trim

To adjust the output voltage downwards, a current source is connected to pin 18. The output voltage decreases when the connected current into pin 8 increases. The current value is given by the equation:

I_{adj}=943 × (1-(V_{od}/V_{oi}))[uA]; V_{oi} is the initial output voltage when pin 8 is disconnected, V_{od} is the desired voltage.

2.5 V / 4.4 A Electrical Specification — PKR4919B SI

T_{ref} = -30 to +95ºC, V_I = 36 to 75 V, pin 8 connected to pin 9 unless otherwise specified under Conditions.
Typical values given at: T_{ref} = +25°C, V_I= 53 V, max I_O, unless otherwise specified under Conditions.

Characteristics		Conditions	min	typ	Max	Unit
V_I	Input voltage range		36		75	V
V_{Ioff}	Turn-off input voltage	Decreasing input voltage	30	33.4	35	V
V_{Ion}	Turn-on input voltage	Increasing input voltage	32	34.4	36	V
C_I	Internal input capacitance			2		µF
P_O	Output power	Output voltage initial setting	0		11	W
SVR	Supply voltage rejection (ac)	f = 100 Hz sine wave, 1 Vp-p		88		dB
η	Efficiency	50 % of max I_O		84		%
		max I_O		84		
		50 % of max I_O , V_I = 48 V		85		
		max I_O , V_I = 48 V		84		
P_d	Power Dissipation	max I_O		2.1	2.7	W
P_{li}	Input idling power	I_O = 0 A, V_I = 53 V		0.61		W
P_{RC}	Input standby power	V_I = 53V (turned off with RC)		57		mW
f_s	Switching frequency	0-100% of max I_O	290	305	325	kHz

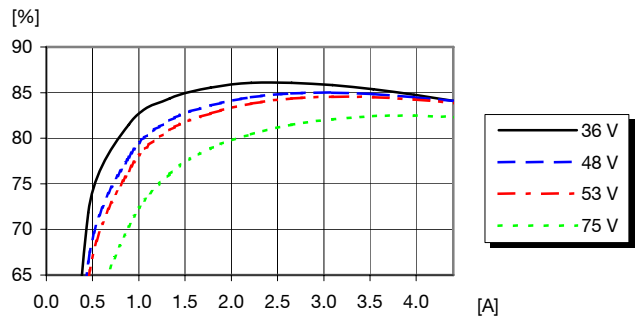
V_{Oi}	Output voltage initial setting and accuracy	T_{ref} = +25°C, V_I = 53 V, max I_O	2.43	2.5	2.56	V
	Output adjust range		2.2		2.8	V
V_O	Output voltage tolerance band	10-100% of max I_O	2.38		2.58	V
	Idling voltage	I_O = 0 A	2.36		2.65	V
	Line regulation	max I_O		14	35	mV
	Load regulation	V_I = 53 V, 10-100% of max I_O		22	116	mV
V_{tr}	Load transient voltage deviation	V_I = 53 V, Load step 25-75-25 % of max I_O, di/dt = 1 A/µs, see Note 1		±220		mV
t_{tr}	Load transient recovery time			0.07		ms
t_r	Ramp-up time (from 10–90 % of V_{oi})	10-100% of max I_O	1	2	9	ms
t_s	Start-up time (from V_I connection to 90% of V_{oi})		2	5	18	ms
I_O	Output current		0		4.4	A
I_{lim}	Current limit threshold	Vo = 2.2 V, T_{ref} < max T_{ref}	5.2	6.1	7	A
I_{sc}	Short circuit current	T_{ref} = 25ºC		6.5	7.5	A
V_{Oac}	Output ripple & noise	See ripple & noise section, max I_O, V_{Oi}		8	20	mVp-p

Note 1: Output filter according to Ripple & Noise section

2.5 V / 4.4 A Typical Characteristics

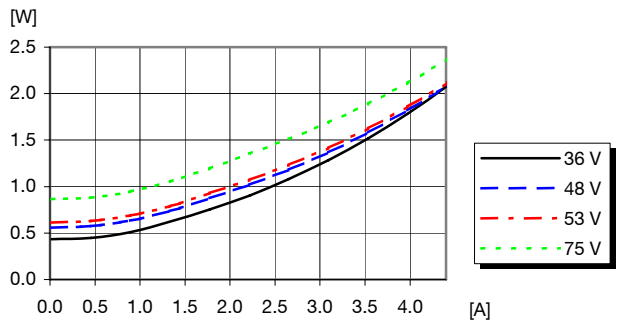
PKR4919B SI

Efficiency



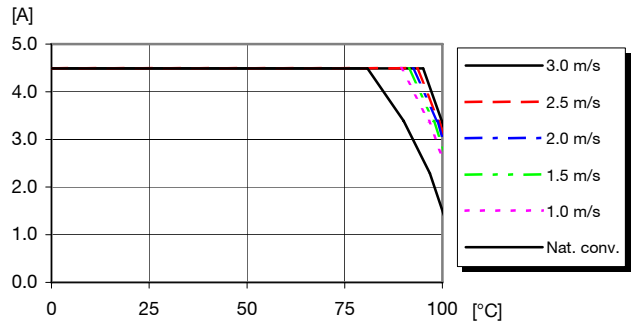
Efficiency vs. load current and input voltage at T_{ref} = +25°C

Power Dissipation



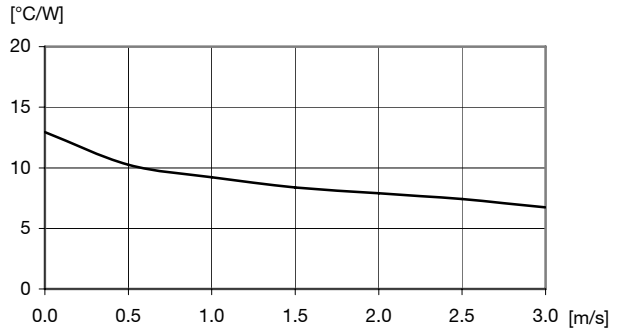
Dissipated power vs. load current and input voltage at T_{ref} = +25°C

Output Current Derating



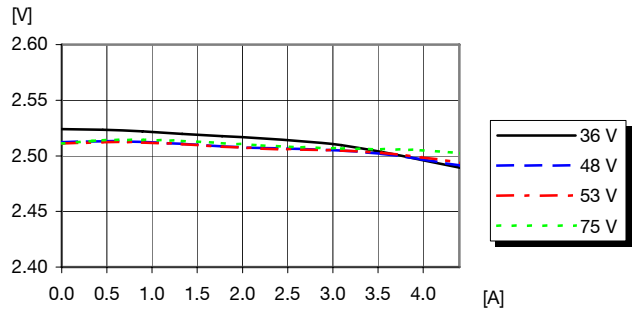
Available load current vs. ambient air temperature and airflow at V_I = 53 V. See Thermal Consideration section.

Thermal Resistance



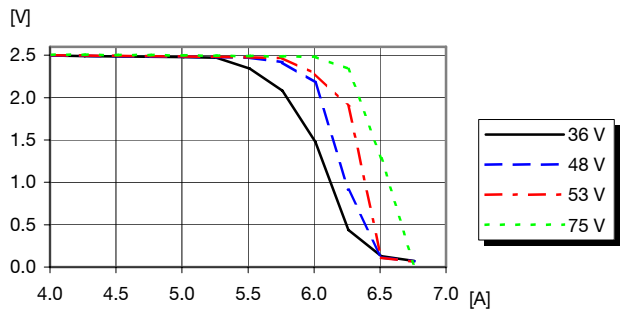
Thermal resistance vs. airspeed measured at the converter. Tested in wind tunnel with airflow and test conditions as per the Thermal consideration section.

Output Characteristics



Output voltage vs. load current at T_{ref} = +25°C

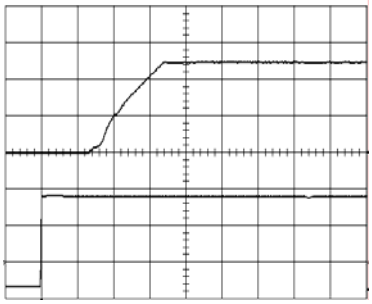
Current Limit Characteristics



Output voltage vs. load current at I_O > max I_O, T_{ref} = +25°C

2.5 V / 4.4A Typical Characteristics — PKR 4919B SI

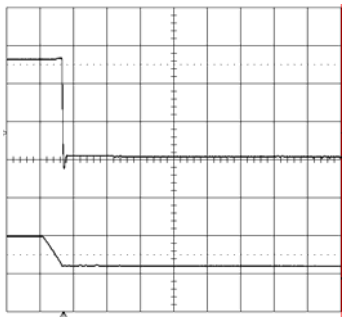
Start-up



Start-up enabled by connecting V_I at: T_{ref} = +25°C, I_O = 4.4 A resistive load, V_I = 53 V.

Top trace: output voltage (1 V/div.). Bottom trace: input voltage (20 V/div.). Time scale: 2 ms/div.

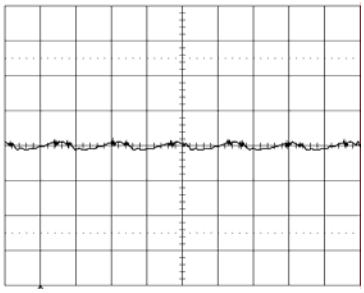
Shut-down



hut-down enabled by disconnecting V_I at: T_{ref} = +25°C, I_O = 4.4 A resistive load, V_I = 53 V.

Top trace: output voltage (1 V/div.). Bottom trace: input voltage (50 V/div.). Time scale: 1 ms/div.

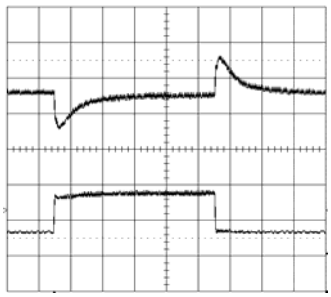
Output Ripple & Noise



Output voltage ripple ({20mV/div.}) at: T_{ref} = +25°C, I_O = 4.4 A resistive load, V_I = 53 V. Time scale: 2 µs/div.

See the filter in the Output ripple and noise section (EMC Specification).

Output Load Transient Response



Output voltage response to load current step-change (1.1-3.3-1.1 A}) at: T_{ref} =+25°C, V_I = 53 V.

Top trace: output voltage (200mV/div). Bottom trace: load current (2 A/div). Time scale: 0.1 ms/div..

Output Voltage Adjust (see operating information)

Passive trim

The resistor value for an adjusted output voltage is calculated by using the following equations:

To adjust the output voltage upwards, a resistor is connected between pins 8 and 17. Pins 8 and 9 have to be shorted. The output voltage increases when the resistance decreases. The resistance value is given by the equation:

Rou= 4.14 × (1.28V_{oi}– V_{od})/(V_{od} – V_{oi}),(kOhm); Vod is the desired output voltage and Voi is the initial output voltage.

Active trim

To adjust the output voltage downwards, a current source is connected to pin 18. The output voltage decreases when the connected current into pin 8 increases. The current value is given by the equation:

I_{adj}=943 × (1-(V_{od}/V_{oi}))[uA]; V_{oi} is the initial output voltage when pin 8 is disconnected, V_{od} is the desired voltage.

3.3 V / 4.5 A Electrical Specification — PKR 4110B SI

T_{ref} = -30 to +95ºC, V_I = 36 to 75 V, pin 8 connected to pin 9 unless otherwise specified under Conditions.
Typical values given at: T_{ref} = +25°C, V_I= 53 V, max I_O, unless otherwise specified under Conditions.

Characteristics		Conditions	min	typ	max	Unit
V_I	Input voltage range		36		75	V
V_{Ioff}	Turn-off input voltage	Decreasing input voltage	30	33.5	35	V
V_{Ion}	Turn-on input voltage	Increasing input voltage	32	34.5	36	V
C_I	Internal input capacitance			2		µF
P_O	Output power	Output voltage initial setting	0		14.85	W
SVR	Supply voltage rejection (ac)	f = 100 Hz sine wave, 1 Vp-p		77		dB
H	Efficiency	50 % of max I_O		85		%
		max I_O		86		
		50 % of max I_O , V_I = 48 V		85		
		max I_O , V_I = 48 V		86		
P_d	Power Dissipation	max I_O		2.5	3.1	W
P_{li}	Input idling power	I_O = 0 A, V_I = 53 V		0.78		W
P_{RC}	Input standby power	V_I = 53 V (turned off with RC)		41		mW
f_s	Switching frequency	0-100% of max I_O	275	305	335	kHz

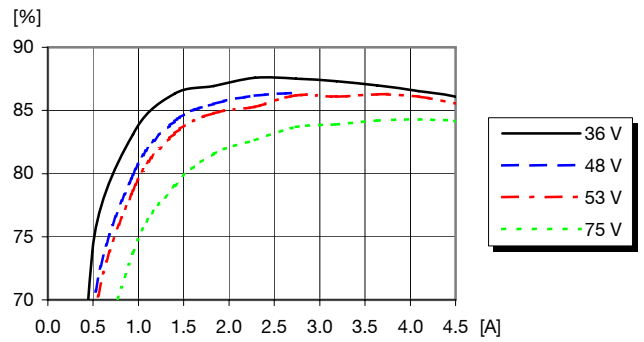
V_{Oi}	Output voltage initial setting and accuracy	T_{ref} = +25°C, V_I =53V, max I_O	3.23	3.30	3.36	V
	Output adjust range		2.80		3.80	V
V_O	Output voltage tolerance band	10-100% of max I_O	3.10		3.43	V
	Idling voltage	I_O = 0 A	3.14		3.47	V
	Line regulation	max I_O		30	50	mV
	Load regulation	V_I = 53 V, 10-100% of max I_O		20	80	mV
V_{tr}	Load transient voltage deviation	V_I = 53 V, Load step 25-75-25 % of max I_O, di/dt = 1 A/µs, see Note 1		±250		mV
t_{tr}	Load transient recovery time			0.10		ms
t_r	Ramp-up time (from 10–90 % of V_{oi})	10-100% of max I_O	1	2	3	ms
t_s	Start-up time (from V_I connection to 90% of V_{oi})		2	6	9	ms
I_O	Output current		0		4.5	A
I_{lim}	Current limit threshold	V_O = 3.0 V, T_{ref} < max T_{ref}	4.6	5.2	5.6	A
I_{sc}	Short circuit current	T_{ref} = 25ºC		5.6	6.6	A
V_{Oac}	Output ripple & noise	See ripple & noise section, max I_O, V_{Oi}		10	20	mVp-p

Note 1: Output filter according to Ripple & Noise section

3.3 V / 4.5 A Typical Characteristics

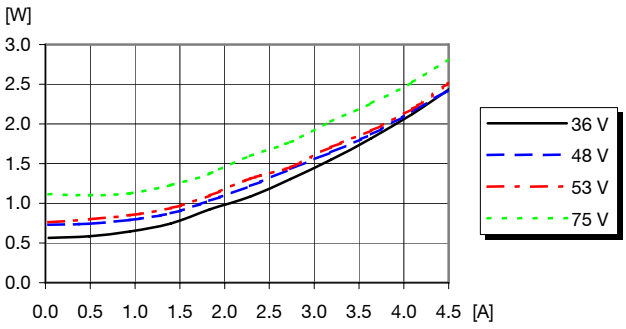
PKR 4110B SI

Efficiency



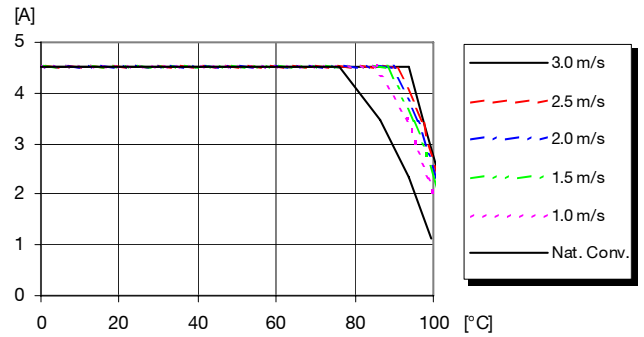
Efficiency vs. load current and input voltage at T_{ref} = +25°C

Power Dissipation



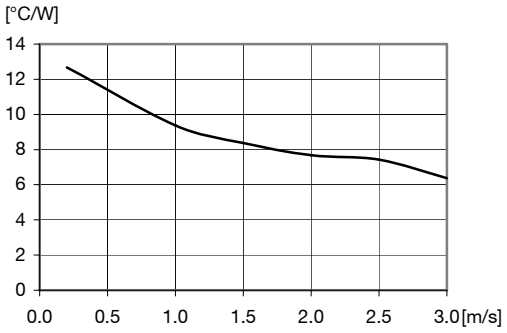
Dissipated power vs. load current and input voltage at T_{ref} = +25°C

Output Current Derating



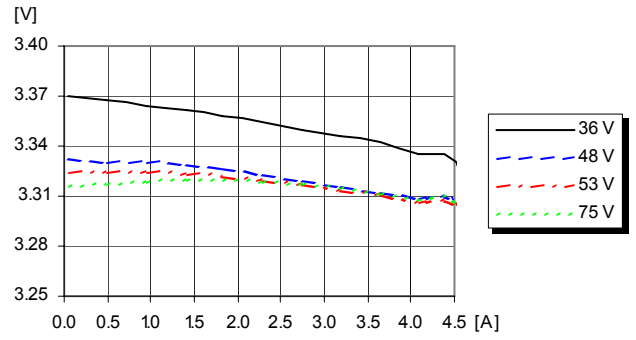
Available load current vs. ambient air temperature and airflow at V_I = 53 V. See Thermal Consideration section.

Thermal Resistance



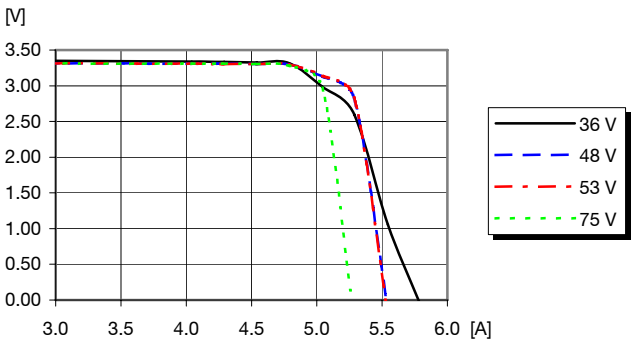
Thermal resistance vs. airspeed measured at the converter. Tested in wind tunnel with airflow and test conditions as per the Thermal consideration section.

Output Characteristics



Output voltage vs. load current at T_{ref} = +25°C

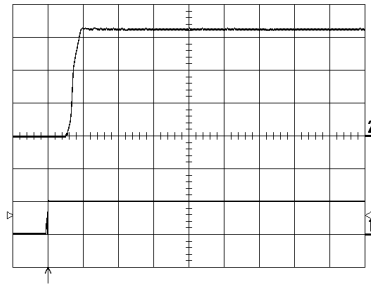
Current Limit Characteristics



Output voltage vs. load current at I_O > max I_O, T_{ref} = +25°C

3.3 V / 4.5 A Typical Characteristics — PKR 4110B SI

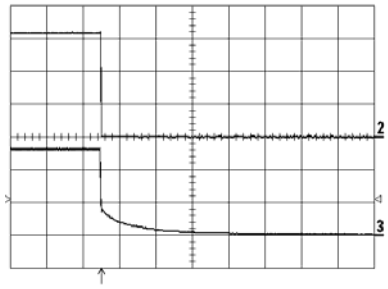
Start-up



Start-up enabled by connecting V_I at: T_{ref} = +25°C, I_O = 4.5 A resistive load, V_I = 53 V.

Top trace: output voltage (1 V/div.). Bottom trace: input voltage (50 V/div.). Time scale: 5 ms/div.

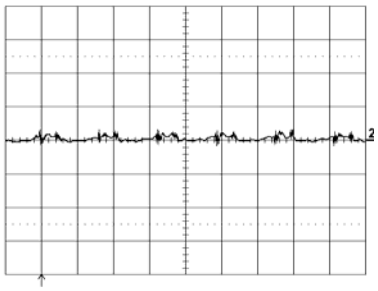
Shut-down



Shut-down enabled by disconnecting V_I at: T_{ref} = +25°C, I_O = 4.5 A resistive load, V_I = 53 V.

Top trace: output voltage (1 V/div.). Bottom trace: input voltage (20 V/div.). Time scale: 0.2 s/div.

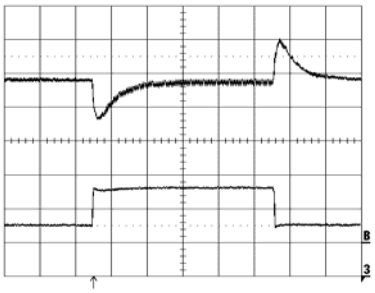
Output Ripple & Noise



Output voltage ripple (20mV/div.) at: T_{ref} = +25°C, I_O = 4.5 A resistive load, V_I = 53 V. Time scale: 2 µs/div.

See the filter in the Output ripple and noise section (EMC Specification).

Output Load Transient Response



Output voltage response to load current step-change (1.12-3.38-1.12 A) at: T_{ref} =+25°C, V_I = 53 V.

Top trace: output voltage (200mV/div.). Bottom trace: load current (2 A/div.). Time scale: 0.1 ms/div.

Output Voltage Adjust (see operating information)

Passive trim

The resistor value for an adjusted output voltage is calculated by using the following equations:

To adjust the output voltage upwards, a resistor is connected between pins 8 and 17. Pins 8 and 9 have to be shorted. The output voltage increases when the resistance decreases. The resistance value is given by the equation:
Rou= 4.14 × (1.28Vi – Vo)/(Vo – Vi),(kOhm); Vo is the desired output voltage and Vi is the initial output voltage.

Active trim

To adjust the output voltage downwards, a current source is connected to pin 18. The output voltage decreases when the connected current into pin 8 increases. The current value is given by the equation:

I_{adj}=943 × (1-(V_{od}/V_{oi}))[uA]; V_i is the initial output voltage when pin 8 is disconnected, V_{od} is the desired voltage.